


KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ ప్రాజెక్టింగ్స్ అండ్ విజ్ఞాన సంస్థానం, వరంగల్ - 506 015 తెలంగాణ, భారత

కాకతీయ సాంకేతిక విజ్ఞాన కార్య విద్యాలయం, వరంగల్ - 506 015 తెలంగాణ, భారతదేశం

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognized by UGC under 2(f) & 12(B); Sponsored by KAKATIYA EDUCATION SOCIETY)

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P: +91 9392055211, +91 7382504868

3ECI

B6-205

 w.e.f.
22.07.2025

ACADEMIC YEAR 2025-26: ODD SEMESTER

Scheme: URR24

B.Tech. Electronics Communication and Instrumentation Engineering

 Class Teacher: **Dr. B. Jeevan**

DAY/TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30		01:30-02:20	02:20-03:10	03:10-04:00
MONDAY	QALR	MFSP	DCLD	AIC&A		OOPJ	S&A	B1: AIC&A(T)-B6- 205 B2: OOPJ(T)-B1-217
TUESDAY	OOP through Java Lab (BJ+BVM+GR) (B1: B1-217- IoT & DA Lab) (B2:B1-219-E-CAD Lab)			ECIA		AIC&A	B1:S&A(T)B1- 217 B2: DCLD(T) B6- 205	B1: QALR(T)-B1- 217/ B2: AIC&A(T)B6- 205
WEDNESDAY	S&A	DCLD	OOPJ	MYC		Programming Skill Development Lab-2 (KSC+KSJ) (B1:B1-219-E-CAD Lab & B2:B1-217- IoT & DA Lab)		
THURSDAY	B1: Analog Integrated Circuits and Applications Lab (KS+KSC+BS) (B1-218- BE & IC Lab) /B2: Sensors & Actuators Lab (BSK+GR) (B1-217- IoT & DA Lab)			B1: DCLD(T)-B1- 218 B2: QALR(T)-B1- 217	LUNCH	MFSP	OOPJ	B1: MFSP(T)-B6- 205 B2: S&A(T)-B1- 217
FRIDAY	MFSP	OOPJ	AIC&A	QALR		DCLD	S&A	B1: OOPJ(T)-B1- 217 B2: MFSP(T)-B6- 205
SATURDAY	B1: Sensors & Actuators Lab (BSK+GR) (B1-217- IoT & DA Lab) / B2: Analog Integrated Circuits and Applications Lab (KS+KSC+BS) (B1-218- BE & IC Lab)			PRACTICUM/ SEA-SAA		-	-	-

Course title in short	Course code & course name		Name of the course faculty
MSFP	U24MH301C	Mathematical foundations for Signal Processing	Prof. M. Sreelatha(MSL)
S&A	U24CI302	Sensors and Actuators	Sri B. Shashikanth (BSK)
AIC&A	U24CI303	Analog Integrated Circuits and Applications	Dr. K. Srinivas (KSC)
DCLD	U24CI304	Digital Circuits and Logic Design	Prof. K. Sivani (KS)
OOPJ	U24CI305	OOP through Java	Dr. B. Jeevan(BJ)
QALR	U24VA306A	Quantitative Aptitude and Logical Reasoning@	Sri G. Dhanush(GD)
PSDLab-2	U24SE307	Programming Skill Development Lab-2	Dr. K. Srinivas (KSC), Smt. K. Shailaja (KSJ)
S&A Lab	U24CI302	Sensors and Actuators Lab	Sri B. Shashikanth, Sri G.Raju
AIC&A Lab	U24CI303	Analog Integrated Circuits and Applications Lab	Prof. K. Sivani (KS), Dr. K. Srinivas (KSC), Smt. B. Smitha (BS)
OOPJ Lab	U24CI305	OOP through Java Lab	Dr. B. Jeevan(BJ), Sri B. Venu Maheswar Rao(BVM), Sri G. Raju(GR)
MYC	MEET YOUR COUNSELLOR		
ECIA	Electronics Communication & Instrumentation Engineering Association		Sri B. Venu Maheswar Rao (BVM) Smt. K. Shailaja (KSJ)
PRACTICUM/SEA-SAA:	Students are required to meet respective PRACTICUM/SEA-SAA supervisors every week during their convenient time (during lunch break/ between 4pm to 5pm)		

 Faculty I/c,
Dept. Timetables

 Faculty I/c,
Overall Timetables

Head of the Department

Dean, AA

Principal

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ACADEMIC YEAR 2025-26: ODD SEMESTER

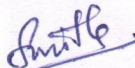
Scheme: URR18-R22

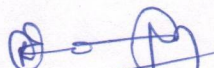
B.Tech. Electronics Communication and Instrumentation Engineering

Class Teacher: **Sri B. Shashikanth**

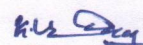
DAY/TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH	01:30-02:20	02:20-03:10	03:10-04:00
MONDAY	B1: Analog and Digital Communications Lab (ESu+CHP) (B6-106 -Communication Lab)/ B2: Embedded Firmware Development Lab (KVM+KSJ+BJ) (B1-201- MPMC Lab)			B1: IoT(T) - B1-217 B2: LIC&A(T) - B1- 201		LIC&A	LCS	B1: MCES(T) - B1-201 B2: LCS(T)-B6 -206
TUESDAY	IoT	ADC	LIC&A	ECIA		B1: Embedded Firmware Development Lab (KVM+KSJ+BJ) (B1-201- MPMC Lab)/ B2: Linear and Digital Integrated Circuits Lab (MSL+BS+GR) (B1-218- BE & IC Lab)		
WEDNESDAY	LIC&A	MCES	UHV-II	MYC		B1: Linear and Digital Integrated Circuits Lab (BS+BVM) (B1-218-BE & IC Lab) / B2: Analog and Digital Communications Lab (ESu+DV) (B6-106 -Communication Lab)		
THURSDAY	ADC	MCES	LCS	B1: ADC(T)-B1- 201 B2: MCES(T)- B6-206		UHV-II	IoT	B1: LCS(T)-B6-206 B2: ADC(T)- B1- 218
FRIDAY	LCS	IoT	MCES	B1: LIC&A(T)- B6-206 B2: IoT(T)-B1- 217		ADC	SEMINAR (KSC+KVM) (B1-201- MPMC Lab)	
SATURDAY	Programming Skill Development Lab (B1: BVM) (B1-219-E-CAD Lab) & (B2: KSJ) (B1-201- MPMC Lab)			LIBRARY				

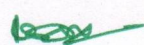
Course title in short		Course code & course name		Name of the course faculty	
UHV-II	U18MH501	Universal Human Values – II		Sri B. Shashikanth (BSK)	
IoT	U18CI502A	Internet of things		Dr. M. Raghu Ram(MRR)	
ADC	U18CI503	Analog and Digital Communications		Smt. E. Sushmitha(ESu)	
LCS	U18EE511	Linear Control Systems		Sri K. Srinivas(KSV)	
MCES	U18CI509	Microcontrollers and Embedded Systems		Prof. K. Venu Madhav (KVM)	
LIC&A	U18CI505	Linear Integrated Circuits and Applications		Prof. M. Sreelatha (MSL)	
EFD LAB	U18CI506	Embedded Firmware Development Laboratory		Prof. K. Venu Madhav (KVM), Smt. K. Shailaja (KSJ), Dr. B. Jeevan(BJ),	
ADC LAB	U18CI507	Analog and Digital Communications Laboratory		Smt. E. Sushmitha (ESu), Dr.D. Venu (DV), Sri Ch. Pavan (CHP)	
LDIC LAB	U18CI508	Linear and Digital Integrated Circuits Laboratory		Prof. M. Sreelatha (MSL), Smt.B.Smitha (BS), Sri B. Venu Maheswar Rao (BVM), Sri G. Raju (GR)	
PSD LAB	Programming Skill Development Lab		Sri B. Venu Maheswar Rao (BVM) Smt. K. Shailaja (KSJ)		
SEMINAR	U18CI510	Seminar		Prof. K. Venumadhav (KVM), Dr. K. Srinivas (KSC)	
MYC	MEET YOUR COUNSELLOR				
ECIA	Electronics Communication & Instrumentation Engineering Association			Sri B. Venu Maheswar(BVM), Smt. K. Shailaja(KSJ)	


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కాకతీయ ప్రాచారికాల్ విజ్ఞాన విశ్వవిద్యాలయం, ఎడ్యుకేషన్ - నల్ల దొన రోడ్డు, వరంగల్

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MONDAY	PM B6- 206	IPC B6- 206	VLSID B6- 206	SC B6- 206		B1: Industrial Process Control Lab (MRR+GR) (B1-104C- IPC Lab)/ B2: Digital Design Lab (OA+RND) (B1-219- E-CAD Lab)		
TUESDAY	VLSID B6- 205	SC B6- 205	B1: IPC(T)- B6- 205 B2: VLSID(T) –B1- 201	ECIA		IPC B6- 206	PM B6- 206	B1:VLSID(T)-B1- 219 B2: SC(T)- B6- 206
WEDNESDAY	B1: Digital Design Lab (OA+RND) (B1-219- E-CAD Lab)/ B2: Industrial Process Control Lab (MRR+GR) (B1-104C- IPC Lab)			MYC		VLSID B6- 205	Internship Evaluation B6- 205	
THURSDAY	SC B6- 205	IPC B6- 205	PM B6- 205	B1: SC(T)- B6- 205 B2: IPC(T)- B1- 219		Training & Placement Practice Session (B1: B1-219- E-CAD Lab) & (B2: B1-201- MPMC Lab)		
FRIDAY	MPW	MPW	MPW	MPW		MPW	MPW	MPW
SATURDAY	MPW	MPW	MPW	MPW		-	-	-

Course title in short	Course code & course name		Name of the course faculty
PM (OE -III)	U18OE701B	Project Management	Sri B. Venu Maheswar (BVM)
SC (PE - III)	U18CI702D	Satellite communications	Sri P. Ramchandrarao (PRR)
VLSID (PE-IV)	U18CI703D	VLSI System Design	Dr. O. Anjaneyulu(OA)
IPC	U18CI 704	Industrial Process Control	Dr. M. Raghu Ram(MRR)
IPC LAB	U18CI 705	Industrial Process Control Laboratory	Dr. M. Raghu Ram (MRR), Sri G. Raju (GR)
DDLAB	U18CI 709	Digital Design Laboratory	Dr. O. Anjaneyulu(OA), Smt. R. Nirmala Devi (RND)
MPW	U18CI 707	Major Project Phase - I	Prof. Prof. M. Sreelatha (MSL), Dr. M.Raghu Ram (MRR)
IE	U18CI 708	Internship Evaluation	Sri B. Shashikanth(BSK)
TPPS	Training & Placement Practice Session		Smt. R. Nirmala Devi (RND)
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