



ACADEMIC YEAR 2025-26: ODD SEMESTER

Scheme: URR24

B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

Class Teacher: Dr. J. Sheshagiri Babu (JSB)

DAY/ TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH BREAK	13.30-14.20	14.20-15.10	15:10-16:00
MONDAY	ECAD	SS	DD	OOP		PSD LAB (PRR + EJ) B6 : 104 & 105		
TUESDAY	QALR	ECAD	SS	B1: SS (T) - B6 :101/ B2: ECAD (T) - B6 :205		AM	OOP	B1: ECAD (T) - B6 : 101 / B2: QALR (T) - B6 : 201
WEDNESDAY	B1: ECAD LAB (MR + VR) B6 : 209/ B2: DD LAB (TSK + EJ) B1 : 206			B1: OOP (T) - B6 :209/ B2: SS (T) - B6 : 205		DD	AM	B1:DD (T) - B6 : 101 / B2: OOP (T) - B6 : 204
THURSDAY	OOP LAB (ES + JTK + CHSR) B6: 104 & 105			ECA		B1: DD LAB (TSK + BRD) B1 :206 / B2: ECAD LAB (MR + EJ) B6 : 209		
FRIDAY	SS	ECAD	QALR	MYC		DD	OOP	B1: QALR (T) - B6 : 101 / B2: AM (T) - B6 : 203
SATURDAY	AM	OOP	B2: DD (T) - B6 : 201/ B1:AM (T) - B6 : 101	PRACTICUM /SEA/SAA				

Course Title	Course Code	Course Name	Course Faculty
AM	U24MH301D	Applied Mathematics	Dr. S. Viswa Prasad Rao (VPR)
ECAD	U24EC302	Electronic Circuits Analysis and Design	Dr. M. Raju (MR)
DD	U24EC303	Digital Design	Dr. B. Rama Devi (BRD)
SS	U24EC304	Signals and Systems	Dr. J. Sheshagiri Babu (JSB)
OOP	U24EC305	OOP through Java#	Sri. E. Suresh (ES)
ECAD	U24EC302	Electronic Circuits Analysis and Design Laboratory	B1 & B2 :Dr. M. Raju (MR) / Sri E. Jawali (EJ)
DD	U24EC303	Digital Design Laboratory	B1: Dr. T. Sunil Kumar (TSK) / Dr. B. Rama Devi (BRD) B2: Dr. T. Sunil Kumar (TSK) / Sri E. Jawali (EJ)
OOP	U24EC307	OOP through Java Laboratory	B1 & B2 : Sri. E. Suresh (ES) / Dr. Ch. Sridevi (CHSR) / Dr. J. Tarun Kumar (JTK)
QALR	U24VA306A	Quantitative Aptitude and Logical Reasoning@	Sri D. Sharath (DS)
PSD	U24SE307	Programming Skill Development Lab - 2	Sri P. Ramchandrarao (PRR) / Sri. E. Jawali (EJ)
PRA-3	U24EL308	Practicum-3	Dr. J. Sheshagiri Babu (JSB)
SEA/SAA	U24VA309	SEA-3 / SAA -3	Sri. Ch. Pavan Kumar (CHPK)
ETS	U24AE310	Expert Talk Series-3	Sri S. Pradeep Kumar (SPK)
MYC	Meet Your Counselor		
	ECA	ECE ASSOCIATION	Dr. B. Dhanalaxmi (BD) Sri. P. Yugander (PY) Dr. Md. Abdul Muqueem (MQ)/ Smt. E.Susmitha
PRACTICUM/SEA-SAA:		Students are required to meet respective PRACTICUM/SEA-SAA supervisors every week during their convenient time (during lunch break/between 4pm to 5pm)	

[Signature]
 Faculty I/c,
 Dept. Timetables

[Signature]
 Head
 Dept. of ECE

[Signature]
 Faculty I/c,
 Overall Timetables

[Signature]
 Dean, AA

[Signature]
 Principal


KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragallu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, TELANGANA, INDIA

కాకతీయ ప్రాధ్యాపనికీ అంబ విజ్ఞాన సంస్థానం, వరంగల్ - 506015, తెలంగాణ, భారత

కృతీకర్త సాంకేతిక విజ్ఞాన సంస్థానం, వరంగల్ - 506015, తెలంగాణ, భారత

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognized by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

website: www.kitsw.ac.in

E-mail: principal@kitsw.ac.in

Phone: +91 9392455231, +91 7392564888

3ECE2
B-VI : 102
w.e.f. 22.07.2025
ACADEMIC YEAR 2025-26: ODD SEMESTER
Scheme: URR24
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING
Class Teacher: Dr. A. Srinivas

DAY/ TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH BREAK	13.30-14.20	14.20-15.10	15:10-16:00
MONDAY	ECAD	DD	SS	B1: OOP (T) - B1 : 205 / B2: DD (T) - B1 : 204		AM	OOP	B1:QALR (T) - B6 : 101 / B2: ECAD (T) - -B6 : 102
TUESDAY	SS	ECAD	DD	B1: AM (T) - B6 : 206 / B2: QALR (T) - B6 : 305		B1: ECAD LAB (BD + VSR) B6 : 209/ B2: DD LAB (AZS + JSB) B1 : 206		
WEDNESDAY	QALR	OOP	AM	SS		PSD LAB (MQ + CHPK) B6 : 104 & 105		
THURSDAY	B1: DD LAB (AZS + JSB) B1 : 206 / B2: ECAD LAB (BD + VSR) B6 : 209			ECA		OOP	QALR	B1: SS (T) - B6 : 201 / B2: OOP (T) - B6 : 102
FRIDAY	OOP LAB (JTK + ES + CHSR) B6: 104 & 105			MYC		DD	ECAD	B1: ECAD (T) - B6 : 102 / B2: SS (T) - B6 : 103
SATURDAY	AM	OOP	B2: AM (T) - B6 : 202/ B1: DD (T) - B6 : 102	PRACTICUM /SEA/SAA				

Course Title	Course Code	Course Name	Course Faculty
AM	U24MH301D	Applied Mathematics	Dr. V. Anand (VA)
ECAD	U24EC302	Electronic Circuits Analysis and Design	Dr. B. Dhanalaxmi (BD)
DD	U24EC303	Digital Design	Dr. A. Srinivas (AZS)
SS	U24EC304	Signals and Systems	Smt. A. Vijaya (AV)
OOP	U24EC305	OOP through Java#	Dr. J. Tarun Kumar (JTK)
QALR	U24VA306A	Quantitative Aptitude and Logical Reasoning@	Sri D. Sharath (DS)
ECAD	U24EC302	Electronic Circuits Analysis and Design Laboratory	B1 & B2: Dr. B. Dhanalaxmi (BD) / Sri. V. Shobhan Reddy (VSR)
DD	U24EC303	Digital Design Laboratory	B1 & B2: Dr. A. Srinivas (AZS) / Dr. J. Sheshagiri Babu (JSB)
OOP	U24EC305	OOP through Java Laboratory	B1 & B2 : Sri. E. Suresh (ES) / Dr. Ch. Sridevi (CHSR) / Dr. J. Tarun Kumar (JTK)
PSD	U24SE307	Programming Skill Development Lab - 2	Dr. MD. Abdul Muqueem (MQ) / Sri. Ch. Pavan Kumar (CHPK)
PRA-3	U24EL308	Practicum-3	Dr. A. Srinivas (AZS)
SEA/SAA	U24VA309	SEA-3 / SAA -3	Dr. B. Dhanalaxmi (BD)
ETS	U24AE310	Expert Talk Series-3	Dr. A. Srinivas (AZS)
MYC	Meet Your Counselor		
	ECA	ECE ASSOCIATION	Dr. B. Dhanalaxmi (BD) Sri. P. Yugander (PY) Dr. Md. Abdul Muqueem (MQ).
PRACTICUM/SEA-SAA:		Students are required to meet respective PRACTICUM/SEA-SAA supervisors every week during their convenient time (during lunch break/between 4pm to 5pm)	

[Signature]
Faculty I/c,
Dept. Timetables

[Signature]
Head
Dept. of ECE

[Signature]
Faculty I/c,
Overall Timetables

[Signature]
Dean, AA

[Signature]
Principal

ACADEMIC YEAR 2025-26: ODD SEMESTER

Scheme: URR24

B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

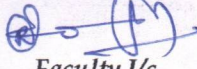
Class Teacher: Dr. K. Sowjanya (KS)

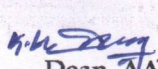
DAY/ TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH BREAK	13.30-14.20	14.20-15.10	15:10-16:00
MONDAY	OOP LAB (CHSR + ES + JTK) B6 : 104 & 105			B1 : QALR (T) - B6 : 105 / B2: AM (T) - B6 : 104		DD	OOP	AM
TUESDAY	B1: ECAD LAB (MC + KS) B6 : 209 / B2: DD LAB (VR + RS) B1 : 206			B1: ECAD (T) - B1 : 205 / B2: DD (T) - B1 : 204		OOP	ECAD	B1: SS (T) - B6 : 103 / B2: OOP (T) - B6 : 202
WEDNESDAY	ECAD	OOP	AM	B2: QALR (T) - B6 : 305 / B1: AM (T) - B6 : 206		QALR	SS	B1: DD (T) - B6 : 206 / B2: ECAD (T) - B6 : 103
THURSDAY	QALR	ECAD	DD	ECA		PSD LAB (BK + RS) B6 : 104 & 105		
FRIDAY	AM	SS	DD	MYC		B1: DD LAB (VR + RS) B1 : 206/ B2: ECAD (MC + KS) B6 : 209		
SATURDAY	SS	OOP	B1: OOP (T) - B6 : 103 / B2: SS (T) - B6 : 203	PRACTICUM /SEA/SAA				

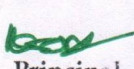
Course Title	Course Code	Course Name	Course Faculty
AM	U24MH301D	Applied Mathematics	Dr. N. Raji Reddy (NRR)
ECAD	U24EC302	Electronic Circuits Analysis and Design	Dr. M. Chandrashekar (MC)
DD	U24EC303	Digital Design	Dr. V. Raju (VR)
SS	U24EC304	Signals and Systems	Dr. K. Sowjanya (KS)
OOP	U24EC305	OOP through Java#	Dr. Ch. Sridevi (CHSR)
QALR	U24VA306A	Quantitative Aptitude and Logical Reasoning@	Sri D. Sharath (DS)
ECAD	U24EC302	Electronic Circuits Analysis and Design Laboratory	B1 & B2: Dr. M. Chandrashekar (MC) / Dr. K. Sowjanya (KS)
DD	U24EC303	Digital Design Laboratory	B1 & B2: Dr. V. Raju (VR) / Dr. R. Srikanth (RS)
OOP	U24EC305	OOP through Java Laboratory	B1 & B2 : Sri. E. Suresh (ES) / Dr. Ch. Sridevi (CHSR) / Dr. J. Tarun Kumar (JTK)
PSD	U24SE307	Programming Skill Development Lab - 2	Sri. B. Komuriah (BK) / Dr. R. Srikanth (RS)
PRA-3	U24EL308	Practicum-3	Dr. K. Sowjanya (KS)
SEA/SAA	U24VA309	SEA-3 / SAA -3	Sri. E. Jawali
ETS	U24AE310	Expert Talk Series-3	Dr. M. Chandrasekhar (MC)
MYC	Meet Your Counselor		
	ECA	ECE ASSOCIATION	Dr. B. Dhanalaxmi (BD) Sri. P. Yugander (PY) Dr. Md. Abdul Muqueem (MQ).
PRACTICUM/SEA-SAA:		Students are required to meet respective PRACTICUM/SEA-SAA supervisors every week during their convenient time (during lunch break/between 4pm to 5pm)	


 Faculty I/c,
 Dept. Timetables


 Head
 Dept. of ECE


 Faculty I/c,
 Overall Timetables


 Dean, AA


 Principal

ACADEMIC YEAR 2025-26: ODD SEMESTER

Scheme: URR18-R22

B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

Class Teacher: Sri. V. Shobhan Reddy (VSR)

DAY/ TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH BREAK	13.30-14.20	14.20-15.10	15:10-16:00
MONDAY	LICA	UHV-II	MPMC	B1: MPMC (T) - B6 : 201/ B2:LICA (T) - B6 : 208		AWP	CS	B2: AWP (T) - B6 : 201 / B1: LICA (T) - B6 : 204
TUESDAY	AIML	LCS	UHV-II	B2: AIML (T) - B6 : 208/ B1 : CS (T) - B6 : 201		PSD LAB (TSK + PC) B6 : 104 & 105		
WEDNESDAY	CS	AIML	AWP	B1: AWP (T) - B6 : 201/ B2: CS (T) - B6 : 208		LICA	LCS	B1: LCS (T) - B6 : 201 / B2: MPMC (T) - B6 : 102
THURSDAY	MPMC	CS	AIML	ECA		B1: ICA LAB (VSR + PRR) B1 : 202/ B2: CS LAB (SPG + BD) B6 : 106		
FRIDAY	B1: CS LAB (SPG + BD) B6 : 106 / B2: ICA LAB (VSR + DSK) B1 : 202			MYC		LICA	LCS	B1: AIML (T) - B6 : 204 / B2: LCS (T) - B6 : 201
SATURDAY	AWP	MPMC	SEMINAR					

Course Title	Course Code	Course Name	Course Faculty
UHV-II	U18MH501	Universal Human Values - II	Sri. M. Anil Kumar (MA)
AIML	U18EC502A	Artificial Intelligence and Machine Learning with Python	Dr. B. Rama Devi (BRD)
CS	U18EC503	Communication Systems	Dr. S. P. Girija (SPG)
AWP	U18EC504	Antennas and Wave Propagation	Dr. M. Chandrashekar (MC)
LICA	U18EC505	Linear Integrated Circuits and Applications	Sri. V. Shobhan Reddy (VSR)
MPMC	U18EC506	Microprocessors and Microcontrollers	Sri. A. Pavan (AP)
LCS	U18EE511	Linear Control Systems	Dr. B. Jagadeesh (BJ)
CS LAB	U18EC507	Communication Systems Laboratory	B1 & B2: Dr. S.P. Girija (SPG) / Dr. B. Dhanalaxmi (BD)
ICA LAB	U18EC508	IC Applications Laboratory	B1: Sri. V. Shobhan Reddy (VSR) / Sri. P. Ramchandar Rao (PRR) B2: Sri. V. Shobhan Reddy (VSR) / Sri. D. Santhosh Kumar (DSK)
SEMINAR	U18EC510	Seminar	Mr. Syed Zaheeruddin (SYZ) / Sri. P. Chiranjeevi (PC) / Dr. T. Sunil Kumar (TSK)
PSD LAB		Programming Skill Development Lab - 3	Dr. T. Sunil Kumar (TSK) / Sri. P. Chiranjeevi (PC)
	ECA	ECE ASSOCIATION	Dr. B. Dhanalaxmi (BD) Sri. P. Yugander (PY) Dr. Md. Abdul Muqeem (MQ) Smt. E.Susmitha (ESU)
MYC	Meet Your Counselor		

[Signature]
 Faculty I/c,
 Dept. Timetables

[Signature]
 Head
 Dept. of ECE

[Signature]
 Faculty I/c,
 Overall Timetables

[Signature]
 Dean, AA

[Signature]
 Principal

ACADEMIC YEAR 2025-26: ODD SEMESTER

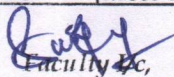
Scheme: URR18-R22

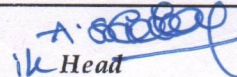
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

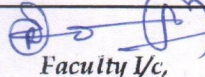
Class Teacher: Dr. MD. Abdul Muqueem (MQ)

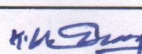
DAY/ TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH BREAK	13.30-14.20	14.20-15.10	15:10-16:00
MONDAY	CS	LICA	AWP	B2: LICA (T) - B1 : 204/ B1: LCS (T) - B6 : 202		MPMC	AIML	B1: AIML (T) - B6 : 202 / B2: CS (T) - B6 : 203
TUESDAY	AWP	LCS	LICA	B1: CS (T) - B6 : 202 / B2: MPMC (T) - B6 : 306		B1: ICA LAB (MQ + ESU) B1 : 202 / B2: CS LAB (AV + PY) B6 : 106		
WEDNESDAY	PSD LAB (SYZ + VSR) B6 : 104 & 105			B1: AWP (T) - B6 : 202/ B2: LCS (T) - B6 : 306		AIML	CS	UHV-II
THURSDAY	B1: CS LAB (AV + PY) B6 : 106 / B2: ICA LAB (MQ + KS) B1 : 202			ECA		LCS	UHV-II	B2: AIML (T) - B6 : 202 / B1: LICA (T) - B6 : 101
FRIDAY	LICA	AIML	CS	MYC		LCS	MPMC	B1: MPMC (T) - B6 : 202 / B2: AWP (T) - B1 : 205
SATURDAY	MPMC	AWP	SEMINAR					

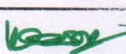
Course Title	Course Code	Course Name	Course Faculty
UHV-II	U18MH501	Universal Human Values - II	Dr. A. Devaraj (DR)
AIML	U18EC502A	Artificial Intelligence and Machine Learning with Python	Dr. G. Raghotham Reddy (GRR)
CS	U18EC503	Communication Systems	Smt. A. Vijaya (AV)
AWP	U18EC504	Antennas and Wave Propagation	Sri. B. Komuraiah (BK)
LICA	U18EC505	Linear Integrated Circuits and Applications	Dr. MD. Abdul Muqueem (MQ)
MPMC	U18EC506	Microprocessors and Microcontrollers	Dr. S. Umamaheshwar (SUM)
LCS	U18EE511	Linear Control Systems	Sri. T. Praveen Kumar (TPK)
CS LAB	U18EC507	Communication Systems Laboratory	B1 & B2: Smt. A. Vijaya (AV) / Sri. P. Yugander (PY)
ICA LAB	U18EC508	IC Applications Laboratory	B1 & B2: Dr. MD. Abdul Muqueem (MQ) / Smt. E. Susmitha (ESU) B2: Dr. MD. Abdul Muqueem (MQ) / Dr. K. Sowjanya (KS)
SEMINAR	U18EC510	Seminar	Dr. M. Chandrashekar (MC)/ Sri. S. Pradeep Kumar (SPK)/ Sri. V. Shobhan Reddy (VSR)
PSD LAB		Programming Skill Development Lab - 3	Mr. Syed Zaheeruddin (SYZ) / Sri. V. Shobhan Reddy (VSR)
	ECA	ECE ASSOCIATION	Dr. B. Dhanalaxmi (BD) Sri. P. Yugander (PY) Dr. Md. Abdul Muqueem (MQ). Smt. E. Susmitha (ESU)
MYC	Meet Your Counselor		


Faculty M/c,
Dept. Timetables


Head
Dept. of ECE


Faculty I/c,
Overall Timetables


Dean, AA


Principal

ACADEMIC YEAR 2025-26: ODD SEMESTER

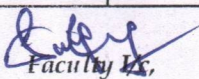
Scheme: URR18-R22

B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

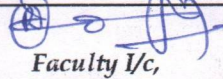
Class Teacher: Sri. B. Komuraiah (BK)

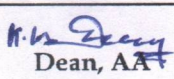
DAY/ TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH BREAK	13.30-14.20	14.20-15.10	15:10-16:00
MONDAY	LCS	AWP	CS	B1: MPMC (T) - B6 : 209/ B2: LCS (T) - B6 : 203		B1: CS LAB (VVR + ESU) B6 : 106/ B2: ICA LAB (BK + MQ) B1 : 202		
TUESDAY	AWP	CS	LCS	B2: AIML (T) - B6 : 105/ B1: LCS (T) - B6 : 203		MPMC	LICA	B2: LICA (T) - B6 : 203/ B1: AIML (T) - B6 : 102
WEDNESDAY	B1: ICA LAB (BK + MQ) B1 : 202 / B2: CS LAB (VVR + ESU) B6 : 106			B2: CS (T) - B6 : 304 / B1: AWP (T) - B6 : 203		UHV-II	LICA	B1: LICA (T) - B6 : 203/ B2: MPMC (T) - B6 : 208
THURSDAY	AIML	LICA	MPMC	ECA		LCS	CS	B1: CS (T) - B6 : 203 / B2: AWP (T) - B6 : 103
FRIDAY	UHV-II	AIML	AWP	MYC		PSD LAB (EJ + DV) B6 : 104 & 105		
SATURDAY	MPMC	AIML	SEMINAR					

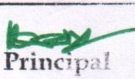
Course Title	Course Code	Course Name	Course Faculty
UHV-II	U18MH501	Universal Human Values - II	Dr. MD. Sameer (MDS)
AIML	U18EC502A	Artificial Intelligence and Machine Learning with Python	Dr. D. Venu (DV)
CS	U18EC503	Communication Systems	Dr. S. Umamaheshwar (SUM)
AWP	U18EC504	Antennas and Wave Propagation	Dr. V. Venkateshwar Reddy (VVR)
LICA	U18EC505	Linear Integrated Circuits and Applications	Sri. B. Komuraiah (BK)
MPMC	U18EC506	Microprocessors and Microcontrollers	Dr. M. Raju (MR)
LCS	U18EE511	Linear Control Systems	Dr. A. Rajashekar (AR)
CS LAB	U18EC507	Communication Systems Laboratory	Dr. V. Venkateshwar Reddy (VVR) / Smt. E. Susmitha (ESU)
ICA LAB	U18EC508	IC Applications Laboratory	Sri. B. Komuraiah (BK) / Dr. MD. Abdul Muqueem (MQ)
SEMINAR	U18EC510	Seminar	Dr. B. Dhanalaxmi (BD) / Sri. B. Komuraiah (BK) / Dr. Ch. Sridevi (SR)
PSD LAB		Programming Skill Development Lab - 3	Smt. E. Jawali (EJ) / Dr. D. Venu (DV)
	ECA	ECE ASSOCIATION	Dr. B. Dhanalaxmi (BD) Sri. P. Yugander (PY) Dr. Md. Abdul Muqueem (MQ). Smt. E. Susmitha (ESU)
MYC	Meet Your Counselor		


 Faculty I/c,
 Dept. Timetables


 Head
 Dept. of ECE


 Faculty I/c,
 Overall Timetables


 Dean, AA


 Principal

ACADEMIC YEAR 2025-26: ODD SEMESTER

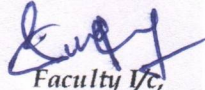
Scheme: URR18-R22

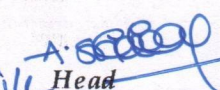
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

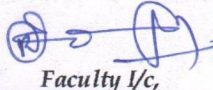
Class Teacher: Dr. T. Sunil Kumar (TSK)

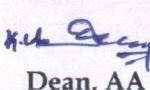
DAY/ TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH BREAK	13.30-14.20	14.20-15.10	15:10-16:00
MONDAY	B1: VLSI LAB (TSK + DSK) B1 : 204/ B2: PPIOT LAB (PRR + MC) B1 : 205			VLSI PD - B6 : 207 / DIP - B6 : 204		MEA	DSE	MYC
TUESDAY	MEA	DSE	DM	VLSI PD - B6 : 207 / DIP - B6 : 204		Major Project Phase - I		
WEDNESDAY	DM	MEA	VLSI PD - B6 : 101 / DIP - B6 : 204	VLSI PD (T) - B6 : 301 / DIP (T)- B6 : 204		B1: PPIOT LAB (PRR + DSK) B1 : 205 / B2: VLSI LAB (TSK + PC) B1 : 204		
THURSDAY	DSE	DM	DSE (T)	ECA		TPPS (KS) (B6 : 208- ADS LAB)		
FRIDAY	Major Project Phase - I					Major Project Phase - I		
SATURDAY	Major Project Phase - I							

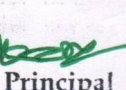
Course Title	Course Code	Course Name	Course Faculty
DM	U18OE701A	Disaster Management	Smt. M. Lakma Kumari (MLK)
DSE	U18EC702A	Data Science Engineering	Dr. T. Sunil Kumar (TSK)
VLSIPD	U18EC703B	VLSI Physical Design	Sri. Ch. Pavan Kumar (CHPK)
DIP	U18EC703C	Digital Image Processing	Dr. K. Sowjanya (KS)
VLSI LAB	U18EC706	VLSI Lab	B1: Dr. T. Sunil Kumar (TSK) / Sri.P. Chiranjeevi (PC) B2: Dr. T. Sunil Kumar (TSK) / Sri. D. Santhosh Kumar (DSK)
MPP-I	U18EC707	Major Project Phase - I	Dr. D. Venu (DV) / Dr. G. Raghotham Reddy (GRR)/ Dr. A. Srinivas (AZS)
IE	U18EC708	Internship Evaluation	Sri. P. Ramchandar Rao (PRR)
PPIOT LAB	U18EC710	Python Programming & IoT Lab	B1: Sri. P. Ramchandar Rao (PRR)/ Dr. M. Chandrashekar (MC) B2: Sri. P. Ramchandar Rao (PRR) / Sri. D. Santhosh Kumar (DSK)
MEA	U18MH711#	Management Economics & Accountancy	Dr. G. Rathnakar (GR)
	TPPS	Training & Placement Practice Session	Dr. K. Sowjanya (KS)
	ECA	ECE ASSOCIATION	Dr. B. Dhanalaxmi (BD) Sri. P. Yugander (PY) Dr. Md. Abdul Muqueem (MQ). Smt. E.Susmitha (ESU)
MYC	Meet Your Counselor		


 Faculty I/c,
 Dept. Timetables


 Head
 Dept. of ECE


 Faculty I/c,
 Overall Timetables


 Dean, AA


 Principal


KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, TELANGANA, INDIA

काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६०१५, तेलंगाना, भारत

కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - 506 015, తెలంగాణ, భారతదేశం

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by KAKATIYA EDUCATION SOCIETY)

website: www.kitsw.ac.in

E-mail: principal@kitsw.ac.in

Phone: 0802055233, 01 732464888

7ECE2

B-VI : 204 & 101 / 102

w.e.f. 22.07.2025

ACADEMIC YEAR 2025-26: ODD SEMESTER
Scheme: URR18-R22
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

Class Teacher: Dr. D. Venu (DV)

DAY/ TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH BREAK	13.30-14.20	14.20-15.10	15:10-16:00
MONDAY	DM (B6 : 204)	DSE (B6 : 204)	MEA (B6 : 204)	DIP - B6 : 102 / VLSIPD - B6 : 207		Major Project Phase - I		MYC
TUESDAY	B1: VLSI LAB (CHPK+ AP) B1 : 204/ B2: PPIOT LAB (DV + GRR) B1 : 205			DIP - B6 : 102 / VLSIPD - B6 : 207		TPPS (PRR) (B1 : 203- DC LAB)		
WEDNESDAY	MEA (B6 :101)	DSE (B6 : 101)	DIP - B6 : 202 / VLSIPD - B6 : 101	DIP (T) - B6 : 103 / VLSIPD (T) - B6 : 301		DM (B6 : 102)	DSE (T) (B6 : 102)	LIBRARY
THURSDAY	MEA (B6 :101)	DM (B6 :101)	DSE (B6 :101)	ECA		B2: VLSI LAB (CHPK + AP) B1 - 204/ B1 : PPIOT LAB (DV + CHSR) B1 - 205		
FRIDAY	Major Project Phase - I					Major Project Phase - I		
SATURDAY	Major Project Phase - I							

Course Title	Course Code	Course Name	Course Faculty
DM	U18OE701A	Disaster Management	Smt. T. Veenaraj (TV)
DSE	U18EC702A	Data Science Engineering	Dr. D. Venu (DV)
VLSIPD	U18EC703B	VLSI Physical Design	Sri. Ch. Pavan Kumar (CHPK)
DIP	U18EC703C	Digital Image Processing	Dr. B. Dhanalaxmi (BD)
MEA	U18MH701	Management, Economics & Accountancy	Dr. K. Jaipal (KJ)
VLSI LAB	U18EC706	VLSI Lab	B1 & B2 : Sri. Ch. Pavan Kumar (CHPK) / Sri. A. Pavan (AP)
MPP-I	U18EC707	Major Project Phase - I	Dr. V. Raju (VR)/ Dr. M. Raju (MR)/ Sri. A. Pavan (AP)
IE	U18EC708	Internship Evaluation	Dr. MD. Abdul Muqueem (MQ)
PPIOT LAB	U18EC710	Python Programming & IoT Lab	B1: Dr. D. Venu (DV) / Dr. G. Raghotham Reddy (GRR) B2: Dr. D. Venu (DV) / Dr. Ch. Sridevi Reddy (CHSR)
MEA	U18MH711#	Management Economics & Accountancy	Faculty A
	TPPS	Training & Placement Practice Session	Sri. P. Ramchandrar Rao (PRR)
	ECA	ECE ASSOCIATION	Dr. B. Dhanalaxmi (BD) Sri. P. Yugander (PY) Dr. Md. Abdul Muqueem (MQ). Smt. E.Susmitha (ESU)
MYC	Meet Your Counselor		

Faculty I/c,

Dept. Timetables

Head

Dept. of ECE

Faculty I/c,

Overall Timetables

Dean, AA

Principal

ACADEMIC YEAR 2025-26: ODD SEMESTER

Scheme: URR18-R22

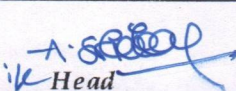
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

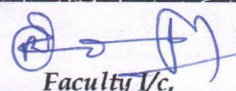
Class Teacher: Sri. P. Yugander (PY)

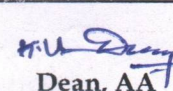
DAY/ TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH BREAK	13.30-14.20	14.20-15.10	15:10-16:00
MONDAY	MEA (B6 : 103)	DM (B6 : 103)	DSE (B6 : 103)	DIP - B6 : 103 / VLSIPD - B6 : 207		TPPS (PY) (B1 : 205-SP LAB)		MYC
TUESDAY	DM (B6 : 103)	MEA (B6 : 103)	DSE (B6 : 103)	DIP - B6 : 103 / VLSIPD - B6 : 207		B1: VLSI LAB (EJ + AP) B1 - 204/ B2: PPIOT LAB (DSK + CHPK) B1 - 205		
WEDNESDAY	DSE (B6 : 202)	MEA (B6 : 202)	DIP - B6 : 203 / VLSIPD - B6 : 101	DIP (T) - B6 : 101 / VLSIPD (T) - B6 : 301		DM (B6 : 204)	DSE (T) (B6 : 204)	LIBRARY
THURSDAY	B2: VLSI LAB (EJ + CHPK) B1 : 204/ B1: PPIOT LAB (DSK + SPK) B1 : 205			ECA		Major Project Phase - I		
FRIDAY	Major Project Phase - I					Major Project Phase - I		
SATURDAY	Major Project Phase - I							

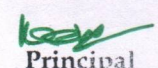
Course Title	Course Code	Course Name	Course Faculty
DM	U18OE701A	Disaster Management	Dr. G. Srinivasa Rao (GSR)
DSE	U18EC702A	Data Science Engineering	Sri. P. Yugander (PY)
VLSIPD	U18EC703B	VLSI Physical Design	Sri. Ch. Pavan Kumar (CHPK)
DIP	U18EC703C	Digital Image Processing	Dr. R. Srikanth (RS)
VLSI LAB	U18EC706	VLSI Lab	B1: Sri E. Jawali / Sri. Ch. Pavan Kumar (CHPK) B2: Sri E. Jawali / Dr. V. Raju (VR)
MPP-I	U18EC707	Major Project Phase - I	Dr. R. Srikanth (RS) / Dr. B. Rama Devi (BRD) / Dr. J. Tarun Kumar (JTK)
IE	U18EC708	Internship Evaluation	Sri. D. Santhosh Kumar (DSK)
PPIOT LAB	U18EC710	Python Programming & IoT Lab	B1 : Sri. D. Santhosh Kumar (DSK) / Sri. S. Pradeep Kumar Sri. D. Santhosh Kumar (DSK) / Sri. Ch. Pavan Kumar (CHPK)
MEA	U18MH711#	Management Economics & Accountancy	Sri. K. Jaipal (KJ)
	TPPS	Training & Placement Practice Session	Sri. P. Yugander (PY)
	ECA	ECE ASSOCIATION	Dr. B. Dhanalaxmi (BD) Sri. P. Yugander (PY) Dr. Md. Abdul Muqueem (MQ). Smt. E.Susmitha (ESU)
MYC	Meet Your Counselor		


 Faculty I/c,
 Dept. Timetables


 Head
 Dept. of ECE


 Faculty I/c,
 Overall Timetables


 Dean, AA


 Principal