

**KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE**

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 505 015, Telangana, INDIA.
 కాకతీయ ప్రాధానికీ అంబ విజ్ఞాన సంస్థానం, వరంగల్ - 505 015, తెలంగాణ, భారత
 (An Autonomous Institute under Kakatiya University, Warangal)
 (Approved by AICTE, New Delhi; Recognized by UGC under 2(f) & 12(B); Sponsored by EKASIA EDUCATION SOCIETY)

2024-25
3ECE1

Room No.
B-VI-101

w.e.f
22-07-2024

ACADEMIC YEAR 2024-25 : ODD SEMESTER**Scheme: URR18-R22****III SEMESTER****B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING**

Class Teacher : Sri. A. Srinivas (AZS)

DAY/TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH	01:30-02:20	02:20-03:10	03:10-04:00
MON	STLD	EM-III	SIS Class (B IV 130)			NA	OOP	AC-I
TUE	EM-III	AC-I LAB (B1) : B-I-206 OOP LAB (B2) : B-I-205 (SP LAB)				PSD LAB B-VI : 104 & 105		
WED	SS	AC-I	EM-III	EM-III (B1) / OOP (B2)		AC-I LAB (B2) : B-I-206 OOP LAB (B1) : B-I-205 (SP LAB)		
THU	SS	STLD	OOP	ECA		AC-I (B1)/ STLD (B2)	OOP (B1) / EM-III (B2)	NA
FRI	OOP	SS	SS (B1) / NA (B2)	MYC		STLD	AC-I	AC-I (B2) / STLD (B1)
SAT	NA	SS (B2) / NA (B1)						

Course title in short		Course code & course name		Name of the course faculty	
EM-III	:	U18MH301	Engineering Mathematics - III	:	Dr. N. Raji Reddy (NRR)
OOP	:	U18OE303	Object Oriented Programming	:	Smt. M. Preethi (MP)
SS	:	U18EC304	Signals & Systems	:	Dr. K. Sowjanya (KS)
AC-I	:	U18EC305	Analog Circuits - I	:	Dr. S.P. Girija (SPG)
STLD	:	U18EC306	Switching Theory & Logic Design	:	Sri. A. Srinivas (AZS)
NA	:	U18EE312	Network Analysis	:	Sri. T. Praveen Kumar (TPK)
AC-I Lab	:	U18EC308	Analog Circuits - I Laboratory	:	Dr. S.P. Girija (SPG)/Sri. A. Srinivas (AZS)
OOP Lab	:	U18OE311	Object Oriented Programming Laboratory	:	B1: Smt. M. Preethi (MP) /Sri. B. Dileep Reddy (BDR) B2:Smt. M. Preethi (MP) /Mrs. E. Manisha (EM)
PSD Lab	:		Programming Softskill Development Laboratory	:	Dr. Ch. Sridevi (CHSR)/ Dr. B. Dhanalaxmi (BD)
SIS	:		Soft and Interpersonal Skills	:	Sri. G. Dhanush (GD)
ECA	:		ECE Association	:	
MYC	:		Meet you counselor	:	

TUTORIAL BATCHES & VENUE**Batch 1= [B23EC001 - B23EC036] Batch 2= [B23EC037 - B23EC064, B24EC193L-199L]**

Course	EM-III	OOP	SS	AC-I	STLD	NA
Batch 1	B-VI-101	B-VI-101	B-VI-101	B-VI-101	B-VI-202	B-VI-204
Batch 2	B-VI-204	B-VI-202	B-VI-101	B-VI-101	B-VI-201	B-VI-103

Faculty I/C,
Dept. Timetables

Faculty I/C,
Overall Timetables

Head of the Department

Dean AA

Principal

**KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE**

Opp : Yerragattu Gutta, Hasanparthy (Mandali), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ టెక్నాలజీ మరియు సైన్స్ విశ్వవిద్యాలయం, వరంగల్ - 506 015 తెలంగాణ, భారతదేశం
Kakatiya Institute of Technology and Science, Warangal - 506 015 Telangana, India

(An Autonomous Institute under Kakatiya University, Warangal)

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ACADEMIC YEAR 2024-25 : ODD SEMESTER**Scheme: URR18-R22****III SEMESTER****B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING****2024-25****3ECE2**Room No.**B-VI-102**w.e.f**22-07-2024**

Class Teacher : Sri A. Pavan

DAY/TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH	01:30-02:20	02:20-03:10	03:10-04:00
MONDAY	OOP	AC-I	SS	EM-III		OOP (B1) / STLD (B2)	SIS Lab (B IV 130)	
TUESDAY	STLD	EM-III	NA	OOP		SS	AC-I (B1) / SS (B2)	NA (B2) / EM III (B1)
WEDNESDAY	AC-I	STLD	NA	AC-I (B2) /STLD (B1)		EM-III	EM-III (B2) / SS (B1)	LIBRARY
THURSDAY	AC-I LAB (B1) : B-I-206 OOP LAB (B2) : B-I-205 (SP LAB)			ECA		STLD	NA	OOP (B2)/ NA (B1)
FRIDAY	SS	OOP	AC-I	MYC		AC-I LAB (B2) : B-I-206 OOP LAB (B1) : B-I-205 (SP LAB)		
SATURDAY	PSD LAB B-VI : 104 & 105							

Course title in short		Course code & course name		Name of the course faculty	
EM-III	: U18MH301	Engineering Mathematics - III	:	Dr. B. Yakaiah (BY)	
OOP	: U18OE303	Object Oriented Programming	:	Sri. P. Srinivas (PS)	
SS	: U18EC304	Signals & Systems	:	Sri. A. Pavan (AP)	
AC-I	: U18EC305	Analog Circuits - I	:	Dr. M. Raju (MR)	
STLD	: U18EC306	Switching Theory & Logic Design	:	Dr. B. Rama Devi (BRD)	
NA	: U18EE312	Network Analysis	:	Dr. G. Sunil Kumar (GSK)	
AC-I Lab	: U18EC308	Analog Circuits - I Laboratory	:	B1&B2 :Dr. M. Raju (MR) / Mr. D. Santhosh Kumar (DSK)	
OOP Lab	: U18OE311	Object Oriented Programming Laboratory	:	B1 & B2: Sri. P. Srinivas (PS) /Sri. R. Rajesh (RR)	
PSD Lab	:	Programming Softskill Development Laboratory	:	Mr. Abdul Muqueem (MQ) / Sri. Ch. Pavan Kumar (CHPK)	
SIS	:	Soft and Interpersonal Skills	:	Ms. P. Sindhura (PS)	
ECA	:	ECE Association	:		
MYC	:	Meet you counselor	:		

TUTORIAL BATCHES & VENUE**Batch 1= [B23EC065 - B23EC100] Batch 2= [B23EC101 - B23EC128, B24EC200L-206L]**

Course	EM-III	OOP	SS	AC-I	STLD	NA
Batch 1	B-VI-101	B-VI-102	B-VI-101	B-VI-101	B-VI-201	B-VI-102
Batch 2	B-VI-102	B-VI-204	B-VI-102	B-VI-102	B-VI-207	B-VI-102

 Faculty I/C,
Dept. Timetables

 Faculty I/C,
Overall Timetables

 Head of the Department

 Dean AA

 Principal

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 కాకతీయ ప్రేక్షానికీ అండ్ విజ్ఞాన సంస్థానం, వరంగల్ - 506 015 తెలంగాణ, భారత
 కౌన్సిల్ ఆఫ్ టెక్నాలజీ విజ్ఞాన సంస్థానం, వరంగల్ - 506 015 తెలంగాణ, భారతదేశం
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2023-24
3ECE3

Room No.
B-VI-103

w.e.f
22-07-2024

ACADEMIC YEAR 2024-25 : ODD SEMESTER**Scheme: URR18-R22****III SEMESTER****B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING**

Class Teacher : Sri. J Sheshagiri Babu (JSB)

DAY/TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH	01:30-02:20	02:20-03:10	03:10-04:00
MONDAY	AC-I LAB (B1) : B-I-206 OOP LAB (B2) : B-I-205 (SP LAB)			OOP		AC-I	STLD (B1)/ SS (B2)	NA
TUESDAY	STLD	SS	AC-I	EM-III		STLD	SIS LAB (B IV 130)	
WEDNESDAY	PSD LAB B-VI : 104 & 105			AC-I (B1) / OOP (B2)		SS	AC-I	EM-III (B2)/ NA (B1)
THURSDAY	EM-III	NA	SS	ECA		OOP	STLD	STLD (B2)/ SS (B1)
FRIDAY	AC-I LAB (B2) : B-I-206 OOP LAB (B1) : B-I-205 (SP LAB)			MYC		EM-III	OOP	AC-I (B2) / OOP (B1)
SATURDAY	NA	EM-III (B1)/ NA (B2)						

Course title in short		Course code & course name		Name of the course faculty	
EM-III	: U18MH301	Engineering Mathematics - III		Smt. K. Nirmala (KN)	
OOP	: U18OE303	Object Oriented Programming		Sri. T. Ranjith Kumar (TRK)	
SS	: U18EC304	Signals & Systems		Sri. J. Sheshagiri Babu (JSB)	
AC-I	: U18EC305	Analog Circuits - I		Dr. V. Venkateshwar Reddy (VVR)	
STLD	: U18EC306	Switching Theory & Logic Design		Dr. R. Srinanth (RS)	
NA	: U18EE312	Network Analysis		Dr. Y. Manjusree (YM)	
AC-I Lab	: U18EC308	Analog Circuits - I Laboratory		B1: Dr. V. Venkateshwar Reddy (VVR) / Sri. V. Shoban Reddy (VSR)/P. Ramchandar Rao B2: Dr. V. Venkateshwar Reddy (VVR) / Sri. V. Shoban Reddy (VSR)/Dr. T. Sunil Kumar	
OOP Lab	: U18OE311	Object Oriented Programming Laboratory		B1 & B2: Sri. T. Ranjith Kumar (TRK) / Sri. B. Dileep Reddy (BDR)	
PSD Lab	:	Programing Softskill Development Laboratory		Dr. R. Srinanth (RS) / Sri. J. Sheshagiri Babu (JSB)	
SIS	:	Soft and Interpersonal Skills		Sri. G. Dhanush (GD)	
ECA	:	ECE Association			
MYC	:	Meet you counselor			

TUTORIAL BATCHES & VENUE

s						
Course	EM-III	OOP	SS	AC-I	STLD	NA
Batch 1	B VI 207	B-VI-103	B-VI-207	B-VI-103	B-VI-103	B-VI-102
Batch 2	B-VI-103	B-VI-203	B-VI-201	B-VI-204	B-VI-103	B-VI-103

Faculty I/C,
Dept. Timetables

Faculty I/C,
Overall Timetables

Head of the Department

Dean AA

Principal

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2024-25

5ECE1

Room No.

B-VI-201

w.e.f

22-07-2024

ACADEMIC YEAR 2024-25 : ODD SEMESTER

Scheme: URR18-R22

V SEMESTER**B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING**

Class Teacher : Dr. Ch. Sridevi

DAY/TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH	01:30-02:20	02:20-03:10	03:10-04:00
MONDAY	LIC	AWP	MPMC	AIML		LIC LAB (B1) -(B-I : 202) / CS LAB (B2) - (B-VI :106)		
TUESDAY	CS	LCS	MPMC	AWP (B1) / LIC (B2)		AIML (B1)/ CS (B2)	AWP	MPMC (B1) / LCS (B2)
WEDNESDAY	UHV II	LIC LAB (B2) -(B-I : 202) / CS LAB (B1) - (B-VI :106)				LCS	CS	MYC
THURSDAY	AIML	LIC	AWP	ECA		PSD LAB B-VI : 104 & 105		
FRIDAY	LIC	CS	MPMC	UHV II		AIML	AWP (B2) / LIC (B1)	MPMC (B2) / LCS (B1)
SATURDAY	LCS	AIML (B2)/ CS (B1)	Seminar					

Course title in short		Course code & course name		Name of the course faculty	
UHV-II	: U18MH501	Universal Human Values - II		: Dr. G. Srinu (GS)	
AIML	: U18EC502	Artificial Intelligence and Machine Learning with Python		: Dr. D. Venu (DV)	
CS	: U18EC503	Communication Systems		: Dr. K. Ramudu (KR)	
AWP	: U18EC504	Antennas and Wave Propagation		: Smt. A. Vijaya (AV)	
LICA	: U18EC505	Linear Integrated Circuits and Applications		: Dr. Ch. Sridevi (CHSR)	
MPMC	: U18EC506	Microprocessors and Microcontrollers		: Dr. S. Umamaheshwar (SUM)	
LCS	: U18EE511	Linear Control Systems		: Dr. B. Jagadeesh Kumar (BJK)	
CS LAB	: U18EC507	Communication Systems Laboratory		: B1 & B2: Dr. K. Sowjanya (KS)/ Dr. K. Ramudu (KR)	
IC Lab	: U18EC508	IC Applications Laboratory		: B1 & B2 :Dr. Ch. Sridevi (CHSR)/ Sri. B. Komuraiah (BK)	
	: U18EC510	Seminar		: Mr. Syed Zaheeruddin (SYZ)	
PSD LAB	:	Programming Skill Development Lab		: Sri. A. Pavan (AP) / Sri. Ch. Pavan Kumar (CHPK)	

TUTORIAL BATCHES & VENUE

Batch 1= [B22EC001 - B22EC037] Batch 2= [B22EC038 - B22EC064, B23EC193L - 199L]

Course	AIML	CS	AWP	LIC	MPMC	LCS
Batch 1	B-VI-201	B-VI-101	B-VI-201	B-VI-203	B-VI-201	B-VI-203
Batch 2	B-VI-201	B-VI-203	B-VI-201	B-I-202	B-VI-201	B-VI-103

Faculty I/C,
Dept. Timetables

Faculty I/C,
Overall Timetables

Head of the Department

Dean AA

Principal

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2024-25

5ECE2

Room No.

B-VI-202

w.e.f

22-07-2024

ACADEMIC YEAR 2024-25 : ODD SEMESTER

Scheme: URR18-R22

V SEMESTER

B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

Class Teacher : Sri. B. Komuraiah (BK)

DAY/TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH	01:30-02:20	02:20-03:10	03:10-04:00
MONDAY	LCS	AIML	LIC	CS		MPMC	MPMC (B1) / LIC (B2)	LCS (B1) / CS (B2)
TUESDAY	CS	AWP	MPMC	UHV II		LCS	AIML (B1)/ LCS (B2)	LIC (B1)/ AWP (B2)
WEDNESDAY	LIC	AIML	CS (B1) / AIML (B2)	MYC		LIC LAB (B1) -(B-I : 202) / CS LAB (B2) - (B-VI :106)		
THURSDAY	MPMC	AIML	LIC	ECA		LCS	UHV II	AWP
FRIDAY	AWP	LIC LAB (B2) -(B-I : 202) / CS LAB (B1) - (B-VI :106)				PSD LAB B-VI : 104 & 105		
SATURDAY	CS	AWP (B1) / MPMC (B2)	Seminar					

Course title in short		Course code & course name		Name of the course faculty	
UHV-II	: U18MH501	Universal Human Values - II		: Sri. P. Anil Kumar (PAK)	
AIML	: U18EC502	Artificial Intelligence and Machine Learning with Python		: Dr. G. Raghotham Reddy (GRR)	
CS	: U18EC503	Communication Systems		: Smt. A. Vijaya (AV)	
AWP	: U18EC504	Antennas and Wave Propagation		: Sri. B. Komuraiah (BK)	
LICA	: U18EC505	Linear Integrated Circuits and Applications		: Sri. E. Suresh (ES)	
MPMC	: U18EC506	Microprocessors and Microcontrollers		: Dr. J. Tarun Kumar (JTK)	
LCS	: U18EE511	Linear Control Systems		: Sri. T. Praveen Kumar (TPK)	
CS LAB	: U18EC507	Communication Systems Laboratory		: B1 & B2: Smt. A. Vijaya (AV) / Sri. Ch. Pavan Kumar (CHPK)	
IC Lab	: U18EC508	IC Applications Laboratory		: B1 : Sri. E. Suresh (ES) / Dr. T. Sunil Kumar (TSK) B2: Sri. E. Suresh (ES) / Mr. Abdul Muqueem (MQ)	
	: U18EC510	Seminar		: Dr. M. Chandrashekar (MC)	
PSD LAB	:	Programming Skill Development Lab		: Dr. B. Dhanalaxmi (BD) / Dr. K. Sowjanya (KS)	

TUTORIAL BATCHES & VENUE

Batch 1= [B22EC065 - B22EC098] Batch 2= [B22EC099 - B22EC128, B23EC200L - 205L]

Course	AIML	CS	AWP	LIC	MPMC	LCS
Batch 1	B-VI-202	B-VI-202	B-VI-202	B-VI-202	B-VI-202	B-VI-202
Batch 2	B-VI-201	B-VI-203	B-VI-203	B-VI-203	B-VI-208	B-VI-203

Faculty I/C,
Dept. Timetables

Faculty I/C,
Overall Timetables

Head of the Department

Dean AA

Principal

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काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६ ०१५ तेलंगाना, भारत

కాకతీయ సాంకేతిక విద్యుత్ శాస్త్ర విశ్వవిద్యాలయం, వరంగల్ - ५०६ ०१५ తెలంగాణ, భారతదేశం

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2024-25

5ECE3

Room No.

B-VI-203

w.e.f

22-07-2024

ACADEMIC YEAR 2024-25 : ODD SEMESTER

Scheme: URR18-R22

V SEMESTER**B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING**

Class Teacher : Dr. M. Chandrashekar (MC)

DAY/TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH	01:30-02:20	02:20-03:10	03:10-04:00
MONDAY	AWP	LIC	UHV-II	LCS		PSD LAB B-VI : 104 & 105		
TUESDAY	CS	MPMC	AIML	LCS (B1)/ AWP (B2)		LIC LAB (B1) -(B-I : 202) / CS LAB (B2) - (B-VI :106)		
WEDNESDAY	LCS	AWP	LIC	MYC		CS	MPMC	MPMC (B1) / CS (B2)
THURSDAY	AIML	CS	AWP	ECA		LIC	AWP (B1) / LCS (B2)	LIC (B1)/ AIML (B2)
FRIDAY	MPMC	UHV II	AIML	AIML (B1)/ LIC (B2)		LIC LAB (B2) -(B-I : 202) / CS LAB (B1) - (B-VI :106)		
SATURDAY	LCS	CS (B1)/ MPMC (B2)	Seminar					

Course title in short		Course code & course name		Name of the course faculty	
UHV-II	: U18MH501	Universal Human Values - II		: Sri. V. Pradeep (VP)	
AIML	: U18EC502	Artificial Intelligence and Machine Learning with Python		: Dr. B. Rama Devi (BRD)	
CS	: U18EC503	Communication Systems		: Dr. S. Umamaheshwar (SUM)	
AWP	: U18EC504	Antennas and Wave Propagation		: Dr. M. Chandrashekar (MC)	
LICA	: U18EC505	Linear Integrated Circuits and Applications		: Dr. J. Tarun Kumar (JTK)	
MPMC	: U18EC506	Microprocessors and Microcontrollers		: Dr. M. Raju (MR)	
LCS	: U18EE511	Linear Control Systems		: Dr. A. Rajashekar (AR)	
CS LAB	: U18EC507	Communication Systems Laboratory		: Dr. M. Chandrashekar (MC)/ Sri. S. Pradeep Kumar (SPK) / Sri. A. Pavan (AP)	
IC Lab	: U18EC508	IC Applications Laboratory		: B1: Dr. J. Tarun Kumar (JTK) / Dr. V. Raju (VR) B2: Dr. J. Tarun Kumar (JTK)/ Sri. B. Komuriah (BK)	
	: U18EC510	Seminar		: Dr. B. Dhanalaxmi (BD)	
PSD LAB	:	Programming Skill Development Lab		: Sri. P. Yugender (PY)/ Dr. D. Venu (DV)	

TUTORIAL BATCHES & VENUE

Batch 1= [B22EC129 - B22EC163] Batch 2= [B21EC174, B22EC164 - B22EC192, B23EC206L - 211L]

Course	AIML	CS	AWP	LIC	MPMC	LCS
Batch 1	B-VI-203	B-VI-203	B-VI-203	B-VI-203	B-VI-203	B-VI-203
Batch 2	B-VI-201	B-VI-204	B-VI-208	B-VI-207	B-I-205	B-VI-201

Faculty I/C,
Dept. Timetables

Faculty I/C,
Overall Timetables

Head of the Department

Dean AA

Principal

ACADEMIC YEAR 2024-25 : ODD SEMESTER
Scheme: URR18-R22
VII SEMESTER
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

2024-25
7ECE1

Room No.
B-VI-204

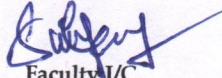
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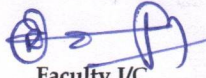
Class Teacher : Dr. B. Dhanalaxmi (BD)

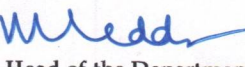
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MONDAY	DSE (B-VI:204)/ RTES (B-VI-104)/ DSD (B-VI-105)	DIP (B-VI-204) / VLSIPD (B-VI-104)	DIP (T) (B-VI-204) / VLSIPD (T) (B-VI-104)	DM (B-VI-204) / PM (B-VI-104)		MEA B-VI:204	Major Project	MYC	
TUESDAY	DM (B-VI-204) / PM (B-VI-104)	DSE (B-VI:204)/ RTES (B-VI-104)/ DSD (B-VI-105)	DSE (T) (B-VI:204)/ RTES (T) (B-VI-104)/ DSD (T) (B-VI-105)	DIP (B-VI-204) / VLSIPD (B-VI-104)		MEA B-VI:204	Major Project		
WEDNESDAY	Major Project					VLSI LAB (B1) -(B-VI: 208) / PPIOT LAB (B2) - (B-VI : 105)			
THURSDAY	Major Project	DIP (B-VI-204) / VLSIPD (B-VI-104)	DSE (B-VI:204)/ RTES (B-VI-104)/ DSD (B-VI-105)	ECA		Major Project			
FRIDAY	DM (B-VI-204) / PM (B-VI-104)	VLSI LAB (B2) -(B-VI: 208) / PPIOT LAB (B1) - (B-VI : 105)				MEA B-VI:204	Internship Evaluation		
SATURDAY									

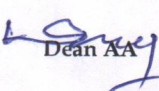
Course title in short		Course code & course name	Name of the course faculty
OE III	: U18OE701A	Disaster Management	: Sri. A. Srinivas (AZS)
	: U18OE701B	Project Management	: Dr. K. Sowjanya (KS)
PE III	: U18EC702A	Data Science Engineering	: Dr. D. Venu (DV)
	: U18EC702B	Real Time Embedded Systems	: Sri. A. Pavan (AP)
	: U18EC702D	Digital System Design	: Sri. Ch. Pavan Kumar (CHPK)
PE IV	: U18EC703B	VLSI Physical Design	: Dr. V. Raju (VR)
	: U18EC703C	Digital Image Processing	: Dr. B. Dhanalaxmi (BD)
MEA	: U18MH711	Management Economics and Accountancy	: Dr. S. Sarika (SS)
VLSI LAB	: U18EC706	VLSI Laboratory	: Dr. V. Raju (VR) / Dr. R. Srinanth (RS)
PPIOT LAB	: U18EC710	Python Programming & IoT Lab	: Dr. D. Venu (DV) / Sri. S. Pradeep Kumar (SPK) / Dr. B. Rama Devi (BRD)
PROJ	: U18EC707	Major Project Phase-I	: Dr. D. Venu (DV)
MC	: U18EC708	Internship Evaluation	: Sri. P. Ramchandar Rao (PRR)
MYC		Meet your counselor	:

TUTORIAL BATCHES & VENUE					
Batch 1= [B21EC001 - B21EC034] Batch 2= [B20EC019, B20EC027, B20EC050, B20EC059, B21EC035 - B21EC064, B22EC195L - 201L]					
Course	DSE	RTES	DSD	VLSI PD	DIP
Batch	B-VI-204	B-VI-104	B-VI-105	B-VI-104	B-VI-204


Faculty I/C,
Dept. Timetables


Faculty I/C,
Overall Timetables


Head of the Department


Dean AA


Principal

2024-25
7ECE2

Room No.
B-VI-207

w.e.f
22-07-2024

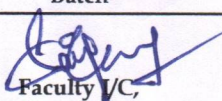
ACADEMIC YEAR 2024-25 : ODD SEMESTER
Scheme: URR18-R22
VII SEMESTER
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

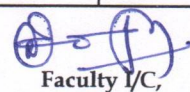
Class Teacher : Sri. Ch. Pavan Kumar (CHPK)

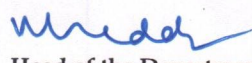
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MONDAY	DSE (B-VI:207)/ RTES (B-VI-104)/ DSD (B-VI-105)	DIP (B-VI-207) / VLSIPD (B-VI-104)	DIP (T) (B-VI-207) / VLSIPD (T) (B-VI-104)	DM (B-VI-207) / PM (B-VI-104)		VLSI LAB (B1) -(B-VI: 208) / PPIOT LAB (B2) - (B-I : 205)		
TUESDAY	DM (B-VI-207) / PM (B-VI-104)	DSE (B-VI:207)/ RTES (B-VI-104)/ DSD (B-VI-105)	DSE (B-VI:207) (T)/ RTES (B-VI-104) (T)/ DSD (B-VI-105) (T)	DIP (B-VI-207) / VLSIPD (B-VI-104)		Major Project		MYC
WEDNESDAY	MEA (B-VI-207)	VLSI LAB (B2) -(B-VI: 208) / PPIOT LAB (B2) - (B-I : 205)				Major Project	Internship Evaluation	
THURSDAY	Major Project	DIP (B-VI-207) / VLSIPD (B-VI-104)	DSE (B-VI:207)/ RTES (B-VI-104)/ DSD (B-VI-105)	ECA		MEA (B-VI-207)	Major Project	
FRIDAY	DM (B-VI-207) / PM (B-VI-104)	MEA (B VI 207)	Major Project			Major Project		
SATURDAY								

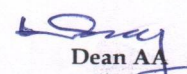
Course title in short		Course code & course name		Name of the course faculty	
OE III	: U18OE701A	Disaster Management		: Mr. Abdul Muqueem (MQ)	
	: U18OE701B	Project Management		: Dr. K. Sowjanya (KS)	
PE III	: U18EC702A	Data Science Engineering		: Sri. P. Yugender (PY)	
	: U18EC702B	Real Time Embedded Systems		: Sri. A. Pavan (AP)	
	: U18EC702D	Digital System Design		: Sri. Ch. Pavan Kumar (CHPK)	
PE IV	: U18EC703B	VLSI Physical Design		: Dr. V. Raju (VR)	
	: U18EC703C	Digital Image Processing		: Dr. R. Srinanth (RS)	
MEA	: U18MH711	Management Economics and Accountancy		: Dr. S. Sarika (SS)	
VLSI LAB	: U18EC706	VLSI Laboratory		: Sri. A. Pavan (AP)/ Sri. Ch. Pavan Kumar (CHPK)	
PPIOT LAB	: U18EC710	Python Programming & IoT Lab		: Sri. A. Srinivas (AZS)/Dr. G. Raghotham Reddy (GRR) / Sri. P. Yugender (PY)	
PROJ	: U18EC707	Major Project Phase-I		: Dr. V. Raju (VR)	
MC	: U18EC708	Internship Evaluation		: Mr. Abdul Muqueem (MQ)	
MYC		Meet your counselor			

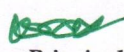
TUTORIAL BATCHES & VENUE					
Batch 1= [B21EC066 - B21EC099] Batch 2= [B20EC072, B20EC106, B20EC126, B21EC100 - B21EC128, B22EC202L - 208L]					
Course	DSE	RTES	DSD	VLSI PD	DIP
Batch	B-VI-207	B-VI-104	B-VI-105	B-VI-104	B-VI-207


Faculty I/C,
Dept. Timetables


Faculty I/C,
Overall Timetables


Head of the Department


Dean AA


Principal

ACADEMIC YEAR 2024-25 : ODD SEMESTER
Scheme: URR18-R22
VII SEMESTER
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

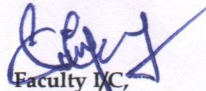
2024-25 7ECE3
Room No. BVI-103
w.e.f 22-07-2024

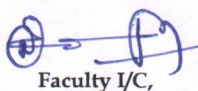
Class Teacher : Dr. T. Sunil Kumar

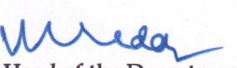
DAY/TIME	09:10-10:00	10:00-10:50	10:50-11:40	11:40-12:30	LUNCH	01:30-02:20	02:20-03:10	03:10-04:00	
MONDAY	DSE (B-VI-103)/ RTES (B-VI-104)/ DSD (B-VI-105)	DIP (B-VI-103) / VLSIPD (B-VI-104)	DIP (T) (B-VI-103) / VLSIPD (T) (B-VI-104)	DM (B-VI-101) / PM (B-VI-104)		Major Project	MEA (B-VI-204)	MYC	
TUESDAY	DM (B-VI-309) / PM (B-VI-104)	DSE (B-VI-101)/ RTES (B-VI-104)/ DSD (B-VI-105)	DSE (T) (B-VI-101)/ RTES (T) (B-VI-104)/ DSD (T) (B-VI-105)	DIP (B-VI-101) / VLSIPD (B-VI-104)		VLSI LAB (B1) -(B-VI: 208) / PPIOT LAB (B2) - (B-I : 205)			
WEDNESDAY	Major Project					Major Project	Internship Evaluation		
THURSDAY	MEA (B-VI-204)	DIP (B-VI-102) / VLSIPD (B-VI-104)	DSE (B-VI-102)/ RTES (B-VI-104)/ DSD (B-VI-105)	ECA		VLSI LAB (B2) -(B-VI: 208) / PPIOT LAB (B1) - (B-I : 205)			
FRIDAY	DM (B-VI-103) / PM (B-VI-104)	Major Project	MEA (B-VI-204)	Major Project		Major Project			
SATURDAY									

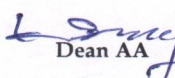
Course title in short		Course code & course name	Name of the course faculty
OE III	: U18OE701A	Disaster Management	: Dr. M. Chandrashekar (MC)
	: U18OE701B	Project Management	: Dr. K. Sowjanya (KS)
PE III	: U18EC702A	Data Science Engineering	: Dr. T. Sunil Kumar (TSK)
	: U18EC702B	Real Time Embedded Systems	: Sri. A. Pavan (AP)
	: U18EC702D	Digital System Design	: Sri. Ch. Pavan Kumar (CHPK)
PE IV	: U18EC703B	VLSI Physical Design	: Dr. V. Raju (VR)
	: U18EC703C	Digital Image Processing	: Dr. K. Ramudu (KR)
MEA	: U18MH711	Management Economics and Accountancy	: Dr. S. Sarika (SS)
VLSI LAB	: U18EC706	VLSI Laboratory	: Dr. T. Sunil Kumar (TSK)/ Mr. Syed Zaheeruddin (SYZ)/ Sri. P. Chiranjeevi (PC)
PPIOT LAB	: U18EC710	Python Programming & IoT Lab	: Mr. D. Santhosh Kumar (DSK)/ Mr. Abdul Muqueem (MQ)
PROJ	: U18EC707	Major Project Phase-I	Dr. R. Srikanth (RS)
MC	: U18EC708	Internship Evaluation	Sri. D. Santhosh Kumar (DSK)
MYC		Meet your counselor	

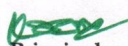
TUTORIAL BATCHES & VENUE					
Batch 1= [B21EC129 - B21EC165] Batch 2= [B21EC166 - B21EC194, B22EC209L - 214L]					
Course	DSE	RTES	DSD	VLSI PD	DIP
Batch	B-VI-103	B-VI-104	B-VI-105	B-VI-104	B-VI-103


 Faculty I/C,
 Dept. Timetables


 Faculty I/C,
 Overall Timetables


 Head of the Department


 Dean AA


 Principal