

29.01.24

2023-24
4ECE1

Room No.
BVI 101

w.e.f
18.12.2023

ACADEMIC YEAR: 2023-24
Scheme: URR18-R22
IV SEMESTER
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

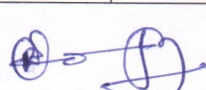
Class Teacher: Dr. Ch. Sridevi

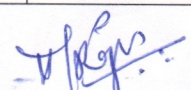
Day	09.10-10.00	10.00-10.50	10.50 -11.40	11:40-12:30		13.30-14.20	14.20-15.10	15:10-16:00
MON	AM	DD	EITK	PRP	L U N C H B R E A K	AC-II LAB (B-I) B-VI 209 / PDC LAB (B-II) B-I 206		
TUE	PRP	AC-II	EMTL	EITK		PDC	DD(T)BI / AC-II(T)BII	AM(T)BI / PDC(T)BII
WED	PDC	PRP	EMTL	DD		AM	AC-II(T)BI / EMTL(T)BII	PRP(T)BI / DD(T)BII
THU	AC-II LAB (B-II) B-VI 209 / PDC LAB (B-I) B-I 206			ECA		PSD LAB B-VI 104 & 105		
FRI	AC-II	PDC	EMTL(T)BI / PRP(T)BII	MYC		PDC(T)BI / AM(T)BII	PE LAB	
SAT	EMTL	AC-II	DD	AM				

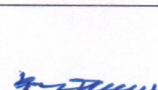
Course Title	Course Code & Course Name	Course Faculty
AM	: U18OE401A: Applicable Mathematics	Dr. E. Ranjith Kumar [ERK]
PE	: U18MH402: Professional English	Dr. W. Grace Shanthi [WGS]
EMTL	: U18EC403: Electro Magnetic Waves and Transmission Lines	Dr. Ch. Sridevi [SR]
AC-II	: U18EC404: Analog Circuits - II	Sri. E. Suresh [ES]
PDC	: U18EC405: Pulse and Digital Circuits	Dr. M. Raju [MR]
PRP	: U18EC406: Probability and Random Processes	Dr. J. Tarun Kumar [JTK]
DD	: U18EC407: Digital Design	Dr. B. Rama Devi [BRD]
EITK	: U18MH415: Essence of Indian Traditional Knowledge	Dr. D. Venu [DV]
AC-II LAB	: U18EC408: Analog Circuits - II Laboratory	Sri. E. Suresh [ES] / Sri. P. Ramchandrarao [PRR]
PDC LAB	: U18EC409: Pulse and Digital Circuits Laboratory	Dr. M. Raju [MR]/ Sri. V. Shobhan Reddy [VSR]
PSD LAB	: Programming & Skill Development Lab	E. Sushmitha (ESU)/ G. Raju (GR)/ K. Shailaja(KSJ)
ES	: U18CH416: Environmental Studies (for Lateral Entry Students Only) (** Every Saturday 3:00pm to 5:00pm online mode)	Dr. G. Sridhar [GS]
MYC	: Meet Your Counselor	

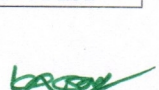
TUTORIAL BATCH COMBINATIONS & VENUES						
Batch 4ECE11= [B22EC001 - 035] Batch 4ECE12= [B22EC036 - 064 & B23EC193L-199L]						
	AM	EMTL	AC-II	PDC	PRP	DD
4ECE11	B-VI 101	B-VI 101	B-VI 101	B-VI 101	B-VI 101	B-VI 101
4ECE12	B-VI 204	B-VI 201	B-VI 309	B-VI 309	B-VI 204	B-VI 201


Faculty I/c,
Dept. of ECE


Faculty I/c,
Overall TT


Head of the Department


Dean, AA


Principal

2023-24
4ECE2

Room No.
BVI 102

w.e.f
18.12.2023

ACADEMIC YEAR: 2023-24
Scheme: URR18-R22
IV SEMESTER
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

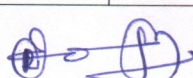
Class Teacher: Sri. B. Komuraiah

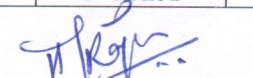
Day	09.10-10.00	10.00-10.50	10.50 -11.40	11:40-12:30		13.30-14.20	14.20-15.10	15:10-16:00
MON	AC-II	AM	PRP	EITK		EMTL	DD	PDC
TUE	PDC	AC-II	PE LAB		L U N C H B R E A K	AC-II LAB (B-II) B-VI 209 / PDC LAB (B-I) B-I 206		
WED	AC-II LAB (B-I) B-VI 209 / PDC LAB (B-II) B-I 206			PRP(T)BI / EMTL(T)BII		EITK	AM(T)BI / AC-II(T)BII	DD(T)BI / PDC(T)BII
THU	DD	PDC	AC-II	ECA		AM	PRP	EMTL(T)BI / PRP(T)BII
FRI	EMTL	DD	PRP	MYC		PSD LAB B-VI 104 & 105		
SAT	AM	EMTL	AC-II(T)BI / AM(T)BII	PDC(T)BI / DD(T)BII				

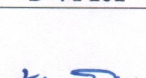
Course Title	Course Code & Course Name	Course Faculty
AM	U18OE401A: Applicable Mathematics	Dr. B. Sandhya Rani [BSR]
PE	U18MH402: Professional English	Dr. D. Vidyanath [DV]
EMTL	U18EC403: Electro Magnetic Waves and Transmission Lines	Dr. M. Chandrashekar [MC]
AC-II	U18EC404: Analog Circuits - II	Dr. V. Venkateshwar Reddy [VVR]
PDC	U18EC405: Pulse and Digital Circuits	Dr. S. Umamaheshwar [SUM]
PRP	U18EC406: Probability and Random Processes	Sri. E. Suresh [ES]
DD	U18EC407: Digital Design	Dr. K. Ramudu [KR]
EITK	U18MH415: Essence of Indian Traditional Knowledge	Sri. B. Komuraiah [BK]
AC-II LAB	U18EC408: Analog Circuits - II Laboratory	Dr. V. Venkateshwar Reddy [VVR] / Sri. A. Srinivas [AZS]
PDC LAB	U18EC409: Pulse and Digital Circuits Laboratory	Sri B. Komuraiah [BK] / Dr. M. Chandrashekar [MC] / Sri. D. Santhosh Kumar [DSK]
PSD LAB	Programming & Skill Development Lab	E. Sushmitha (ESU)/ G. Raju (GR)/ K. Shailaja(KSJ)
ES	U18CH416: Environmental Studies (for Lateral Entry Students Only) (** Every Saturday 3:00pm to 5:00pm online mode)	Dr. G. Sridhar [GS]
MYC	Meet Your Counselor	

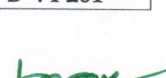
TUTORIAL BATCH COMBINATIONS & VENUES						
Batch 4ECE21= [B22EC065 - 098] Batch 4ECE22= [B22EC099 - 128 & B23EC200L-205L]						
	AM	EMTL	AC-II	PDC	PRP	DD
4ECE21	B-VI 102	B-VI 102	B-VI 102	B-VI 102	B-VI 102	B-VI 102
4ECE22	B-VI 201	B-VI 205	B-VI 204	B-VI 204	B-VI 101	B-VI 201


Faculty I/c,
Dept. of ECE


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Overall TT


Head of the Department


Dean, AA


Principal

2023-24
4ECE3

Room No.
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ACADEMIC YEAR: 2023-24
Scheme: URR18-R22
IV SEMESTER
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

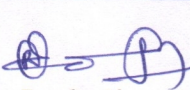
Class Teacher: Sri. S. Pradeep Kumar

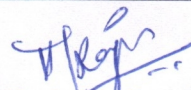
Day	09.10-10.00	10.00-10.50	10.50 -11.40	11.40-12.30	13.30-14.20	14.20-15.10	15.10-16.00
MON	AC-II LAB (B-I) B-VI 209 / PDC LAB (B-II) B-I 206			DD(T)BI / PRP(T)BII	PDC	PRP	EMTL
TUE	PRP	PSD LAB B-VI 104 & 105			DD	AC-II	AM
WED	DD	AC-II	PE LAB		EITK	PDC(T)BI / DD(T)BII	PRP(T)BI / EMTL(T)BII
THU	EMTL	PRP	AM	ECA	AC-II LAB (B-II) B-VI 209 / PDC LAB (B-I) B-I 206		
FRI	AC-II	PDC	EMTL	MYC	EITK	AM(T)BI / AC-II(T)BII	AC(T)BI / PDC(T)BII
SAT	PDC	DD	AM	EMTL(T)BI / AM(T)BII			

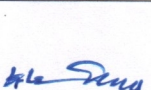
Course Title	Course Code & Course Name	Course Faculty
AM	: U18OE401A: Applicable Mathematics	Dr. D. Rajaiah [DR]
PE	: U18MH402: Professional English	Smt. T. Vinaraj [TV]
EMTL	: U18EC403: Electro Magnetic Waves and Transmission Lines	Sri. A. Srinivas [AZS]
AC-II	: U18EC404: Analog Circuits – II	Dr. S. Umamaheshwar [SUM]
PDC	: U18EC405: Pulse and Digital Circuits	Sri. P. Chiranjeevi [PC]
PRP	: U18EC406: Probability and Random Processes	Dr. Ch. Sridevi [SR]
DD	: U18EC407: Digital Design	Sri. A. Vijaya [AV]
EITK	: U18MH415: Essence of Indian Traditional Knowledge	Sri. S. Pradeep Kumar [SPK]
AC-II LAB	: U18EC408: Analog Circuits - II Laboratory	Dr. S. Umamaheshwar [SUM] / Dr. V. Raju [VR]
PDC LAB	: U18EC409: Pulse and Digital Circuits Laboratory	Sri P. Chiranjeevi [PC] / Dr. K. Ramudu [KR]
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ES	U18CH416: Environmental Studies (for Lateral Entry Students Only) (** Every Saturday 3:00pm to 5:00pm online mode)	Dr. G. Sridhar [GS]
MYC	Meet Your Counselor	

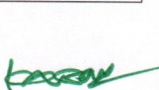
TUTORIAL BATCH COMBINATIONS & VENUES						
Batch 4ECE31= [B22EC129 - 164] Batch 4ECE32= [B23EC165 - 192 & B23EC206L - 211L & B21EC174]						
	AM	EMTL	AC-II	PDC	PRP	DD
4ECE31	B-VI 103	B-VI 103	B-VI 103	B-VI 103	B-VI 103	B-VI 103
4ECE32	B-VI 202	B-VI 205	B-VI 204	B-VI 204	B-VI 205	B-VI 205


Faculty I/c,
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6ECE1

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ACADEMIC YEAR: 2023-24
Scheme: URR18-R22
VI SEMESTER
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

Class Teacher: Dr. B. Dhanalaxmi

Day	09.10-10.00	10.00-10.50	10.50 -11.40	11:40-12:30	L U N C H B R E A K	13.30-14.20	14.20-15.10	15:10-16:00
MON	MPES LAB (B-I) B-I 204 / ADS LAB (B-II) B-VI 104			IIOT (T) / WSN (T)		VLSI	DSPA	VLSI(T)BI / QALR(T)BII
TUE	DSPA	ADS	IIOT / WSN B-VI 102	IIOT / WSN B-VI-102		QALR	QALR(T)BI / ADS(T)BII	ADS(T)BI / ESARM(T)BI I
WED	IIOT / WSN B-VI 102	ADS	ESARM	DSPA(T)BI / VLSI(T)BII		ADS LAB (B-I) B-VI 104 / DSP LAB (B-II) B-I 205		
THU	VLSI	ESARM	DSPA	ECA		DSP LAB (B-I) B-I 205 / MPES LAB (B-II) B-I 204		
FRI	ADS	VLSI	ESARM	MYC		QALR	ESARM(T) BI / DSPA(T)BII	MP
SAT			MP					

Course Title	Course Code & Course Name	Course Faculty
QALR	U18TP601: Quantitative Aptitude & Logical Reasoning	Sri. D. Sharath (DS)
IIOT	U18EC603A: Industrial Internet of Things	Dr. B. Rama Devi (BRD)
WSN	U18EC603B: Wireless Sensor Networks	Dr. B. Dhanalaxmi (BD)
DSPA	U18EC604: Digital Signal Processing and Applications	Dr. R. Srikanth (RS)
VLSI	U18EC605: VLSI Circuits and Systems	Dr. V. Venkateshwar Reddy (VVR)
ESARM	U18EC606: Embedded Systems with ARM Processor and Applications	Dr. G. Raghotham Reddy (GRR)
ADS	U18CS611: Advanced Data Structures	Dr. V. Chandrashekar Rao (VCR)
DSP LAB	U18EC608: Digital Signal Processing Laboratory	Dr. R. Srikanth (RS)/ Dr. B. Dhanalaxmi (BD)
ADS LAB	U18CS612: Advanced Data Structures laboratory	Dr. V. Chandrashekar Rao (VCR)/R. Rajesh(RR)
MPES LAB	U18EC613: Microprocessor and Embedded Systems laboratory	Sri. Ch. Pavan Kumar (CHPK) / Dr. G. Raghotham Reddy (GRR) / Sri. A. Pavan (AP)
MP	U18EC610: Mini Project	Sri. P. Chiranjeevi (PC)
MYC	Meet Your Counselor	

TUTORIAL BATCH COMBINATIONS & VENUES

Batch 6ECE11= [B21EC001 – 037] Batch 6ECE12= [B21EC038 - 064 & B22EC195L - 201L & B20EC027, 039 & 059]

	QALR	IIOT	WSN	DSPA	VLSI	ESARM	ADS
6ECE11	B-VI 201	B-VI 201	B-VI 204	B-VI 201	B-VI 201	B-VI 201	B-VI 201
6ECE12	B-VI 204			B-VI 202	B-VI 204	B-VI 308	B-VI 308

Faculty I/c,
Dept. of ECE

Faculty I/c,
Overall TT

Head of the Department

Dean, AA

Principal

2023-24
6ECE2

Room No.
BVI 202

w.e.f
18.12.2023

ACADEMIC YEAR: 2023-24
Scheme: URR18-R22
VI SEMESTER
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

Class Teacher: Dr. V. Raju

Day	09.10-10.00	10.00-10.50	10.50 -11.40	11.40-12.30		13.30-14.20	14.20-15.10	15:10-16:00
MON	DSPA	ESARM	ADS	IIOT (T) / WSN (T)		VLSI	QALR	VLSI(T)BI / DSPA(T)BII
TUE	ADS	DSPA	IIOT / WSN B-VI 102	IIOT / WSN B-VI 102	L U N C H	ADS LAB (B-I) B-VI 104 / MPES LAB (B-II) B-I 204		
WED	IIOT / WSN B-VI 102	QALR	VLSI	ESARAM		QALR(T)BI / VLSI(T)BII	ESARM(T)BI / QALR(T)BII	MP
THU	ADS LAB (B-II) B-VI 104 / DSP LAB (B-I) B-I 205			ECA	B R E A K	DSPA	ADS(T)BI / ESARM(T)BII	DSPA(T)BI / ADS(T)BII
FRI	ESARM	VLSI	ADS	MYC		MPES LAB (B-I) B-I 204 / DSP LAB (B-II) B-I 205		
SAT				MP				

Course Title	Course Code & Course Name	Course Faculty
QALR	U18TP601: Quantitative Aptitude & Logical Reasoning	Sri. D. Sharath (DS)
IIOT	U18EC603A: Industrial Internet of Things	Dr. S. P. Girija (SPG)
WSN	U18EC603B: Wireless Sensor Networks	Dr. B. Dhanalaxmi
DSPA	U18EC604: Digital Signal Processing and Applications	Smt. A. Vijaya (AV)
VLSI	U18EC605: VLSI Circuits and Systems	Dr. V. Raju (VR)
ESARM	U18EC606: Embedded Systems with ARM Processor and Applications	Dr. J. Tarun Kumar (JTK)
ADS	U18CS611: Advanced Data Structures	Sri. N. Deepika (ND)
DSP LAB	U18EC608: Digital Signal Processing Laboratory	Smt. A. Vijaya (AV) /Dr. B. Rama Devi(BRD)/ Sri. P. Yugender (PY)
ADS LAB	U18CS612: Advanced Data Structures laboratory	Sri N. Deepika (ND) / R. Radhika(RRK) /P. Manasa Raj(PMR)
MPES LAB	U18EC613: Microprocessor and Embedded Systems laboratory	Dr. J. Tarun Kumar (JTK) / Dr. Ch. Sridevi Reddy (SR)
MP	U18EC610: Mini Project	Sri. S. Pradeep Kumar (SPK)
MYC	Meet Your Counselor	

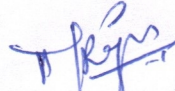
TUTORIAL BATCH COMBINATIONS & VENUES

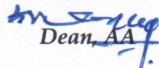
Batch 6ECE21= [B21EC065 - 099] Batch 6ECE22= [B21EC100 - 128 & B22EC202L - 208L & B20EC072 & B20EC106]

	QALR	IIOT	WSN	DSPA	VLSI	ESARM	ADS
6ECE21	B-VI 202	B-VI 202	B-VI 204	B-VI 202	B-VI 202	B-VI 202	B-VI 202
6ECE22	B-VI 304			B-VI 205	B-VI 304	B-VI 201	B-VI 201


Faculty I/c,
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Faculty I/c,
Overall TT


Head of the Department


Dean, AA


Principal

2023-24
6ECE3

Room No.
BVI 203

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ACADEMIC YEAR: 2023-24
Scheme: URR18-R22
VI SEMESTER
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

Class Teacher: Dr. D. Venu

Day	09.10-10.00	10.00-10.50	10.50 -11.40	11:40-12:30		13.30-14.20	14.20-15.10	15:10-16:00
MON	DSPA	ESARM	QALR	IIOT (T) / WSN (T)		ADS LAB (B-I) B-VI 104 / MPES LAB (B-II) B-I 204		
TUE	QALR	DSPA	IIOT / WSN B-VI 102	IIOT / WSN B-VI 102	L U N C H	VLSI	VLSI(T)BI / DSPA (T)BII	DSPA(T)BI / ADS (T)BII
WED	IIOT / WSN B-VI 102	ADS LAB (B-II) B-VI 104 / DSP LAB (B-I) B-I 205				VLSI	ADS	ADS(T)BI / ESARM (T)BII
THU	ADS	ESARM	QALR (T)BI / VLSI (T)BII	ECA	B R E A K	DSPA	ESARM (T)BI / QALR (T)BII	MP
FRI	MPES LAB (B-I) B-I 204 / DSP LAB (B-II) B-I 205			MYC		VLSI	ESARM	ADS
SAT			MP					

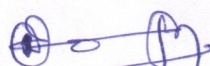
Course Title	Course Code & Course Name	Course Faculty
QALR	U18TP601: Quantitative Aptitude & Logical Reasoning	Sri. D. Sharath (DS)
IIOT	U18EC603A: Industrial Internet of Things	Dr. G. Raghotham Reddy (GRR)
WSN	U18EC603B: Wireless Sensor Networks	Dr. B. Dhanalaxmi
DSPA	U18EC604: Digital Signal Processing and Applications	Dr. D. Venu (DV)
VLSI	U18EC605: VLSI Circuits and Systems	Dr. K. Ramudu (KR)
ESARM	U18EC606: Embedded Systems with ARM Processor and Applications	Dr. S. P. Girija (SPG)
ADS	U18CS611: Advanced Data Structures	Sri. B. Sridhara Murthy (BSM)
DSP LAB	U18EC608: Digital Signal Processing Laboratory	Dr. D. Venu (DV) / Dr. K. Sowjanya (KS)
ADS LAB	U18CS612: Advanced Data Structures laboratory	Sri. B. Sridhara Murthy (BSM) / V. Gouthami (VG)/P. Manasa Raj(PM)
MPES LAB	U18EC613: Microprocessor and Embedded Systems laboratory	Dr. S. P. Girija (SPG) / Mr. Abdul Muqueem (MQ)
MP	U18EC610: Mini Project	Dr. Ch. Sridevi Reddy (SR)
MYC	Meet Your Counselor	

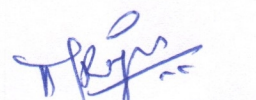
TUTORIAL BATCH COMBINATIONS & VENUES

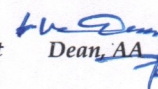
Batch 6ECE31= [B21EC129 - B21EC165] Batch 6ECE32= [B21EC166 - 194 & B22EC209L - 214L]

	QALR	IIOT	WSN	DSPA	VLSI	ESARM	ADS
6ECE31	B-VI 203	B-VI 203	B-VI 204	B-VI 203	B-VI 203	B-VI 203	B-VI 203
6ECE32	B-VI 205			B-VI 307	B-VI 202	B-VI 202	B-VI 307


Faculty I/c,
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8ECE1

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ACADEMIC YEAR: 2023-24
Scheme: URR18-R22
VIII SEMESTER
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

Class Teacher: Sri. A. Srinivas

Day	09.10-10.00	10.00-10.50	10.50 -11.40	11:40-12:30	L U N C H B R E A K	13.30-14.20	14.20-15.10	15:10-16:00
MON	CMC /DSP (B-VI-201)/ MN (B-I-202)		ED / OR (B-VI-201)	LIB		RSC / FPGA (B-VI-203)		
TUE	RSC / FPGA (B-I-202)	RSC / FPGA (B-I-202)– (T)	CMC /DSP (B-VI-208)/ MN (B-I-202)	CMC/DSP/ MN – (T)		ED / OR (B-VI-102)		
WED	PROJ					PROJ		
THU	PROJ			ECA		PROJ		
FRI	PROJ					PROJ		

Course Title	Course Code & Course Name	Course Faculty
FPGA	U18EC801B: FPGA-Based System Design	Sri. Ch. Pavan Kumar (CHPK)
RSC	: U18EC801C: Radar and Satellite Communication	Dr. B. Dhanalaxmi (BD)
CMC	: U18EC802A: Cellular and Mobile Communication System	Sri. A. Srinivas (AZS)
MN	U18EC802B: MEMs and NEMs	Sri. P. Ramchandrar Rao (PRR)
DSP	: U18EC802C: Digital Speech Processing	Sri. S. Pradeep Kumar (SPK)
OR	U18OE803A: Operations Research	Sri. V. Shobhan Reddy (VSR)
ED	: U18OE803C: Entrepreneurship Development	Sri. A. Pavan (AP)
PROJ	U18EC804: Major Project - Phase - II	Dr. R. Srikanth (RS)
MYC	Meet Your Counselor (**Every Monday during Lunch Break)	

TUTORIAL BATCH COMBINATIONS & VENUES					
Batch 8ECE1 = []					
	CMC	DSP	MN	RSC	FPGA
8ECE1	B-VI 204	B-VI-208	B-I-202	B-VI 204	B-I-202

Faculty I/c,
Dept. of ECE

Faculty I/c,
Overall TT

Head of the Department

Dean, AA

Principal

2023-24
8ECE2

Room No.
BVI 205

w.e.f
18.12.2023

ACADEMIC YEAR: 2023-24
Scheme: URR18-R22
VIII SEMESTER
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

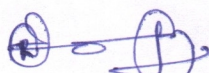
Class Teacher: Dr. K. Sowjanya

Day	09.10-10.00	10.00-10.50	10.50 -11.40	11:40-12:30	L U N C H B R E A K	13.30-14.20	14.20-15.10	15:10-16:00
MON	CMC /DSP (B-VI-201)/ MN (B-I-202)		ED / OR (B-VI-201)	LIB		RSC / FPGA (B-VI-203)		
TUE	RSC / FPGA (B-I-202)	RSC / FPGA (B-I-202)– (T)	CMC /DSP (B-VI-208)/ MN (B-I-202)	CMC/ DSP (B-VI-208)/ MN (B-I-202) – (T)		ED / OR (B-VI-102)		
WED	PROJ					PROJ		
THU	PROJ			ECA		PROJ		
FRI	PROJ					PROJ		

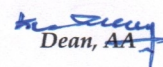
Course Title	Course Code & Course Name	Course Faculty
FPGA	U18EC801B: FPGA-Based System Design	Sri. Ch. Pavan Kumar (CHPK)
RSC	: U18EC801C: Radar and Satellite Communication	Dr. K. Sowjanya (KS)
CMC	: U18EC802A: Cellular and Mobile Communication System	Sri. J. Sheshagiri Babu (JSB)
MN	U18EC802B: MEMs and NEMs	Sri. P. Ramchandrarao (PRR)
DSP	: U18EC802C: Digital Speech Processing	Sri. S. Pradeep Kumar (SPK)
OR	U18OE803A: Operations Research	Sri. V. Shobhan Reddy (VSR)
ED	: U18OE803C: Entrepreneurship Development	Dr. M. Chandrashekar (MC)
PROJ	U18EC804: Major Project - Phase - II	Dr. V. Raju (VR)
MYC	Meet Your Counselor (** Every Monday during Lunch Break)	

TUTORIAL BATCH COMBINATIONS & VENUES					
Batch 8ECE2 = []					
	CMC	DSP	MN	RSC	FPGA
8ECE2	B-VI 205	B-VI-208	B-I-202	B-VI 205	B-I-202


Faculty I/c,
Dept. of ECE


Faculty I/c,
Overall TT


Head of the Department


Dean, AA


Principal

2023-24
8ECE3

Room No.

w.e.f
18.12.2023

ACADEMIC YEAR: 2023-24
Scheme: URR18-R22
VIII SEMESTER
B.Tech. ELECTRONICS AND COMMUNICATION ENGINEERING

Class Teacher: Sri. V. Shobhan Reddy

Day	09.10-10.00	10.00-10.50	10.50 -11.40	11:40-12:30	13.30-14.20	14.20-15.10	15:10-16:00
MON	CMC (B-VI-103)/ DSP (B-VI-201)/ MN (B-I-202)		ED (B-VI-103)/ OR (B-VI-201)	LIB	RSC (B-VI-101)/ FPGA (B-VI-203)		
TUE	RSC (B-VI-106)/ FPGA (B-I-202)	RSC (B-VI-106)/ FPGA (B-I-202)– (T)	CMC (B-VI-103)/ DSP (B-VI-208)/ MN (B-I-202)	CMC (B-VI-103)/ DSP (B-VI-208)/ MN (B-I-202) – (T)	ED (B-VI-202)/ OR (B-VI-102)		
WED	PROJ				PROJ		
THU	PROJ			ECA	PROJ		
FRI	PROJ				PROJ		

Course Title	Course Code & Course Name	Course Faculty
FPGA	U18EC801B: FPGA-Based System Design	Sri. Ch. Pavan Kumar (CHPK)
RSC	: U18EC801C: Radar and Satellite Communication	Sri. B. Komuraiah (BK)
CMC	: U18EC802A: Cellular and Mobile Communication System	Dr. K. Sowjanya (KS)
MN	U18EC802B: MEMs and NEMs	Sri. P. Ramchandrarao (PRR)
DSP	: U18EC802C: Digital Speech Processing	Sri. S. Pradeep Kumar (SPK)
OR	U18OE803A: Operations Research	Sri. V. Shobhan Reddy (VSR)
ED	: U18OE803C: Entrepreneurship Development	Dr. R. Srikanth (RS)
PROJ	U18EC804: Major Project - Phase - II	Dr. D. Venu (DV)
MYC	Meet Your Counselor (** Every Monday during Lunch Break)	

TUTORIAL BATCH COMBINATIONS & VENUES

Batch 8ECE3 = []

	CMC	DSP	MN	RSC	FPGA
8ECE3	B-VI 103	B-VI-208	B-I-202	B-VI 106	B-I-202

Faculty I/c,
Dept. of ECE

Faculty I/c,
Overall TT

Head of the Department

Dean, AA

Principal