



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ ప్రేక్షాగికి అం విజ్ఞాన సంస్థాన, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారత
కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారతదేశము

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

website: www.kitsw.ac.in

E-mail: principal@kitsw.ac.in

☎ : +91 9392055211, +91 7382564888

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2021-22

III SEMESTER of B. Tech ECE(I)

Class Teacher: E.Suresh

w.e.f: 13.09.2021

Day/Time		09.40-10.30	10.30-11.20	11.20-12.10	12.10-13.0	L U N C H B R E A K	14.00 -14.50	14.50-15.40	15.40-16.30
Monday	Course	EM-III	STLD	SIS			AC-I	OOP	LIBRARY
	Faculty	ERK	ES	GRK			KR	PS	
	Venue	B-VI-101	B-VI-101	B-IV-130			B-VI-101	B-VI-101	
Tuesday	Course	SS	AC-I LAB(B-I)/OOP LAB(B-II)				STLD(T)-B-I /AC-I(T)-B-II	EM-III	LIBRARY
	Faculty	DV	KR,ESu, JSB/PS,BSM				ES/KR	ERK	
	Venue	B-VI-101	B-I-206/B-I-205				B-VI-101/ B-VI-201	B-VI-101	
Wednesday	Course	AC-I	SS	NA	EM-III		OOP	STLD	LIBRARY
	Faculty	KR	DV	BPK	ERK		PS	ES	
	Venue	B-VI-101	B-VI-101	B-VI-101	B-VI-101		B-VI-101	B-VI-101	
Thursday	Course	OOP	AC-I(T)-B-I /STLD(T)-B-II	EM-III(T)-B-I /SS(T)-B-II	ECEA		AC-I LAB(B-II)/OOP LAB(B-I)		
	Faculty	PS	KR/ES	ERK/DV			KR,ESu,AT/PS,BSM		
	Venue	B-VI-101	B-VI-101/ B-VI-103	B-VI-101/ B-VI-103			B-I-206/ B-I-205		
Friday	Course	STLD	AC-I	NA(T)-B-I /OOP(T)-B-II	LIBRARY		SS	OOP(T)-B-I /NA(T)-B-II	SS(T)-B-I/ EM-III(T)-B-II
	Faculty	ES	KR	BPK/PS			DV	PS/BPK	DV/ERK
	Venue	B-VI-101	B-VI-101	B-VI-101/ B-VI-102			B-VI-101	B-VI-101/ B-VI-204	B-VI-101/ B-VI-204
Saturday	Course	EM-III	NA						
	Faculty	ERK	BPK						
	Venue	B-VI-101	B-VI-101						

T: Tutorial, B-I: Batch-I, B-II: Batch-II

Course Title	Course Code & Course Name	Course Faculty
EM-III	U18MH301: Engineering Mathematics-III	Dr.E.Ranjith Kumar (ERK)
SIS	U18TP302: Soft and Interpersonal Skills	G.Rakesh Yadhav (GRY)
OOP	U18OE303A: Object Oriented Programming	P.Srinivas (PS)
SS	U18EC304: Signals & Systems	D.Venu (DV)
AC-I	U18EC305: Analog Circuits -I	Dr.K.Ramudu (KR)
STLD	U18EC306: Switching Theory & Logic Design	E.Suresh (ES)
NA	U18EE312: Network Analysis	Dr.B.Pradeep Kumar (BPK)
AC-I LAB	U18EC308: Analog Circuits -I Laboratory	Dr.K.Ramudu (KR)/ E.Susmitha(ESu),J.Sheshagiri Babu(JSB),Ayesha Tabassum(AT)
OOP LAB	U18OE311A: Object Oriented Programming Lab	P.Srinivas (PS)/B.Sridhara Murthy (BSM)

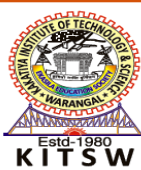
Sd/-
Faculty I/c,
Dept.Time Tables

Sd/-
Faculty I/c,
Overall Time Tables

Sd/-
Head of the
Department

Sd/-
Dean, AA

Sd/-
Principal



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ ప్రేక్షాగిక్రీ ఁవ్ విజ్ఞాన సంస్థాన, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారత్

కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారత్

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

website: www.kitsw.ac.in

E-mail: principal@kitsw.ac.in

☎ : +91 9392055211, +91 7382564888

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2021-22

III SEMESTER of B. Tech ECE (II)

Class Teacher: Dr.Agarwal Ashok

w.e.f: 13.09.2021

Day/Time		09.40-10.30	10.30-11.20	11.20-12.10	12.10-13.0	L U N C H B R E A K	14.00 -14.50	14.50-15.40	15.40-16.30
Monday	Course	SS	OOP	AC-I	STLD		OOP	LIBRARY	LIBRARY
	Faculty	AA	KJ	AZS	VR		KJ		
	Venue	B-VI-102	B-VI-102	B-VI-102	B-VI-102		B-VI-102		
Tuesday	Course	SIS		SS	AC-I		STLD	EM-III	NA
	Faculty	GRK		AA	AZS		VR	TRR	AR
	Venue	B-IV-130		B-VI-102	B-VI-102		B-VI-102	B-VI-102	B-VI-102
Wednesday	Course	AC-I	NA	EM-III(T)- B-I/SS(T)- B-II	STLD		AC-I LAB(B-I)/OOP LAB(B-II)		
	Faculty	AZS	AR	ERK/DV	VR		AZS,CHPK,VR/KJ,VCR		
	Venue	B-VI-102	B-VI-102	B-VI-102/ B-VI-203	B-VI-102		B-I-206/ B-I-205		
Thursday	Course	EM-III	SS	OOP	ECEA		STLD(T)-B-I /AC-I(T)-B-II	NA(T)-B-I /OOP(T)-B-II	LIBRARY
	Faculty	TRR	AA	KJ			VR/AZS	AA/KJ	
	Venue	B-VI-102	B-VI-102	B-VI-102			B-VI-102/ B- VI-101	B-VI-102/ B-VI-101	
Friday	Course	SS	AC-I LAB(B-II)/OOP LAB(B-I)				AC-I(T)-B-I /STLD(T)-B-II	EM-III	LIBRARY
	Faculty	AA	AZS,CHPK,RS/KJ,VCR				AZS/VR	TRR	
	Venue	B-VI-102	B-I-206/ B-I-205				B-VI-102/ B-VI-205	B-VI-102	
Saturday	Course	OOP(T)-B-I /NA(T)-B-II	EM-III						
	Faculty	KJ/AA	TRR						
	Venue	B-VI-102/ B-VI-204	B-VI-102						

T: Tutorial, B-I: Batch-I, B-II: Batch-II

Course Title	Course Code & Course Name	Course Faculty
EM-III	U18MH301: Engineering Mathematics-III	Dr.T.Ragunatha Rao (TRR)
SIS	U18TP302: Soft and Interpersonal Skills	G.Rakesh Yadhav (GRY)
OOP	U18OE303A: Object Oriented Programming	K.Johnson (KJ)
SS	U18EC304: Signals & Systems	Dr.Agarwal Ashok (AA)
AC-I	U18EC305: Analog Circuits -I	A.Srinivas (AZS)
STLD	U18EC306: Switching Theory & Logic Design	Dr.V.Raju (VR)
NA	U18EE312: Network Analysis	Dr.A.Rajshekar (AR)
AC-I LAB	U18EC308: Analog Circuits -I Laboratory	A.Srinivas (AZS)/ Ch.Pavan Kumar(CHPK)/Dr.V.Raju(VR), R.Srikanth(RS)
OOP LAB	U18OE311A: Object Oriented Programming Lab	K.Johnson (KJ)/V.Chandrashekar Rao (VCR)

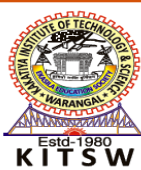
Sd/-
Faculty I/c,
Dept.Time Tables

Sd/-
Faculty I/c,
Overall Time Tables

Sd/-
Head of the
Department

Sd/-
Dean, AA

Sd/-
Principal



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६ ०१५ तेलंगाना, भारत

కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారతదేశము

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

website: www.kitsw.ac.in

E-mail: principal@kitsw.ac.in

☎ : +91 9392055211, +91 7382564888

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2021-22

III SEMESTER of B. Tech ECE(III)

Class Teacher: A.Vijaya

w.e.f: 13.09.2021

Day/Time		09.40-10.30	10.30-11.20	11.20-12.10	12.10-13.0		14.00 -14.50	14.50-15.40	15.40-16.30
Monday	Course	STLD	AC-I LAB(B-I)/OOP LAB(B-II)				SS	EM-III	OOP
	Faculty	AP	TS,DSR.KS/ER,GS				AV	NRR	ER
	Venue	B-VI-103	B-I-206/ B-I-205				B-VI-103	B-VI-103	B-VI-103
Tuesday	Course	NA	OOP	SIS			SS	AC-I(T)-B-I /EM-III(T)-B-II	EM-III
	Faculty	KS	ER	GRK			AV	TS/NRR	NRR
	Venue	B-VI-103	B-VI-103	B-IV-130			B-VI-103	B-VI-103/ B-VI-201	B-VI-103
Wednesday	Course	AC-I	SS(T)-B-I /STLD(T)-B-II	EM-III	LIBRARY		AC-I	NA(T)-B-I /OOP(T)-B-II	LIBRARY
	Faculty	TS	AV/AP	NRR			TS	KS/ER	
	Venue	B-VI-103	B-VI-103/ B-VI-203	B-VI-103			B-VI-103	B-VI-103/ B-VI-102	
Thursday	Course	AC-I LAB(B-II)/OOP LAB(B-I)			ECEA		STLD	STLD(T)-B-I /SS(T)-B-II	LIBRARY
	Faculty	TS,DSR,BNK/ER,GS					AP	AP/AV	
	Venue	B-I-206/ B-I-205					B-VI-103	B-VI-103/ B-VI-206	
Friday	Course	STLD	AC-I	SS	NA		OOP	EM-III	LIBRARY
	Faculty	AP	TS	AP	KS		ER	NRR	
	Venue	B-VI-103	B-VI-103	B-VI-103	B-VI-103		B-VI-103	B-VI-103	
Saturday	Course	EM-III(T)-B-I /AC-I(T)-B-II	OOP(T)-B-I /NA(T)-B-II						
	Faculty	NRR/TS	NRR/TS						
	Venue	B-VI-103/ B-VI-205	B-VI-103/ B-VI-205						

T: Tutorial, B-I: Batch-I, B-II: Batch-II

Course Title	Course Code & Course Name	Course Faculty
EM-III	U18MH301: Engineering Mathematics-III	Dr.N.Raji Reddy (NRR)
SIS	U18TP302: Soft and Interpersonal Skills	G.Rakesh Yadhav (GRY)
OOP	U18OE303A: Object Oriented Programming	E.Rajitha (ER)
SS	U18EC304: Signals & Systems	A.Vijaya (AV)
AC-I	U18EC305: Analog Circuits -I	T.Sunil Kumar(TS)
STLD	U18EC306: Switching Theory & Logic Design	A.Pavan (AP)
NA	U18EE312: Network Analysis	K.Srinivas
AC-I LAB	U18EC308: Analog Circuits -I Laboratory	T.Sunil Kumar(TS)/D.Srinivasa Rao(DSR),Dr.K.Sowjanya(KS), B.Neerajakshi(BNK)
OOP LAB	U18OE311A: Object Oriented Programming Lab	E.Rajitha (ER)/G.Sridhar (GS)

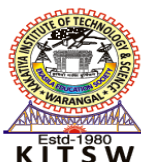
Sd/-
Faculty I/c,
Dept.Time Tables

Sd/-
Faculty I/c,
Overall Time Tables

Sd/-
Head of the
Department

Sd/-
Dean, AA

Sd/-
Principal



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ ప్రైవేట్ టెక్నాలజీస్ & సైన్స్, వరంగల్ - 506 015 తెలంగాణ, భారత్
కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - 506 015 తెలంగాణ, భారత్

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

website: www.kitsw.ac.in

E-mail: principal@kitsw.ac.in

☎ : +91 9392055211, +91 7382564888

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2021-22

V SEMESTER of B. Tech ECE(I)

Class Teacher: S.P.Girija

w.e.f: 13.09.2021

Day/Time		09.40-10.30	10.30-11.20	11.20-12.10	12.10-13.0		14.00 -14.50	14.50-15.40	15.40-16.30
Monday	Course	AIML	MPMC LAB(B-I)/CS LAB(B-II)				MPMC	LICA	AWP
	Faculty	RS	SPG,AA/VK,RS,BNK				SPG	PC	AV
	Venue	B-VI-201	B-I-204/B-VI-106				B-VI-201	B-VI-201	B-VI-201
Tuesday	Course	AWP	MPMC	AIML	CS		IC LAB(B-I)/MPMC LAB(B-II)		
	Faculty	AV	SPG	RS	BRD		PC,PY,SPK/SPG,AA		
	Venue	B-VI-201	B-VI-201	B-VI-201	B-VI-201		B-I-202/B-I-204		
Wednesday	Course	LICA	AIML	UHV-II	AWP	L U N C H B R E A K	MPMC(T)-B-I /AIML(T)-B-II	CS(T)-B-I /AWP(T)-B-II	LIBRARY
	Faculty	PC	RS	GRR	AV		SPG/RS	BRD/AV	
	Venue	B-VI-201	B-VI-201	B-VI-201	B-VI-201		B-VI-201/ B-I-001A	B-VI-201/ B-I-001A	
Thursday	Course	CS LAB(B-I)/IC LAB(B-II)			ECEA		LICA(T)-B-I /MPMC(T)-B-II	CS	LIBRARY
	Faculty	VK,RS/PC,PY					PC/SPG	BRD	
	Venue	B-I-206/B-I-202					B-VI-201/ B-I-001A	B-VI-201	
Friday	Course	MPMC	LICA	CS	AWP(T)-B-I /LICA(T)-B-II		UHV-II	AWP(T)-B-I /CS(T)-B-II	LIBRARY
	Faculty	SPG	PC	BRD	AV/PC		GRR	AV/BRD	
	Venue	B-VI-201	B-VI-201	B-VI-201	B-VI-201/ B-VI-202		B-VI-201	B-VI-201/ B-VI-205	
Saturday	Course	SEMINAR							
	Faculty								
	Venue								

T: Tutorial, B-I: Batch-I, B-II: Batch-II

Course Title	Course Code & Course Name	Course Faculty
UHV-II	U18MH501: Universal Human Values-II	Dr.G.Ragotham Reddy (GRR)
AIML	U18EC502A: Artificial Intelligence and Machine Learning with Python	R.Srikanth (RS)
CS	U18EC503: Communication Systems	Dr.B.Ramadevi (BRD)
AWP	U18EC504: Antennas and Wave Propagation	A.Vijaya (AV)
LICA	U18EC505: Linear Integrated Circuits and Applications	P.Chiranjeevi (PC)
MPMC	U18EC506: Microprocessors and Microcontrollers	S.P.Girija (SPG)
CS LAB	U18EC507: Communication Systems Laboratory	Dr.V.Kishore(VK)/ R.Srikanth (RS),B.Neerajakshi(BNK)
IC LAB	U18EC508: IC Applications Laboratory	P.Chiranjeevi (PC)/ P.Yugander (PY), S.Pradeep Kumar (SPK)
MPMC LAB	U18EC509: Microprocessors and Microcontrollers Laboratory	S.P.Girija (SPG)/Dr.Agarwal Ashok(AA)
SEMINAR	U18EC510: Seminar	

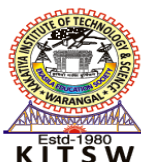
Sd/-
Faculty I/c,
Dept.Time Tables

Sd/-
Faculty I/c,
Overall Time Tables

Sd/-
Head of the
Department

Sd/-
Dean, AA

Sd/-
Principal



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ ప్రైవేట్ టెక్నాలజీ మరియు విజ్ఞాన సంస్థానం, వరంగల్ - 506 015 తెలంగాణ, భారత
కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - 506 015 తెలంగాణ, భారతదేశము

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

website: www.kitsw.ac.in

E-mail: principal@kitsw.ac.in

☎ : +91 9392055211, +91 7382564888

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2021-22

V SEMESTER of B. Tech ECE(II)

Class Teacher: Dr. V. Venkateswar Reddy

w.e.f: 13.09.2021

Day/Time		09.40-10.30	10.30-11.20	11.20-12.10	12.10-13.0	L U N C H B R E A K	14.00 -14.50	14.50-15.40	15.40-16.30
Monday	Course	AIML	CS	MPMC	AWP		LICA	UHV-II	LIBRARY
	Faculty	DSK	MR	GRR	VVR		SPK	VR	
	Venue	B-VI-202	B-VI-202	B-VI-202	B-VI-202		B-VI-202	B-VI-202	
Tuesday	Course	AWP	MPMC LAB(B-I)/CS LAB(B-II)				AIML	CS	MPMC
	Faculty	VVR	BK,SSP/MR,VVR				DSK	MR	GRR
	Venue	B-VI-202	B-I-204/B-VI-106				B-VI-202	B-VI-202	B-VI-202
Wednesday	Course	MPMC	LICA	CS(T)-B-I /AWP(T)-B-II	AIML		IC LAB(B-I)/MPMC LAB(B-II)		
	Faculty	GRR	SPK	MR/VVR	DSK		SPK,DSK,PY/BK,SSP,PR		
	Venue	B-VI-202	B-VI-202	B-VI-202/ B-I-206	B-VI-202		B-I-202/B-I-204		
Thursday	Course	LICA	CS	MPMC(T)-B-I /AIML(T)-B-II	ECEA		AIML(T)-B-I /CS(T)-B-II	AWP(T)-B-I /LICA(T)-B-II	LIBRARY
	Faculty	SPK	MR	GRR/DSK			DSK/MR	VVR/SPK	
	Venue	B-VI-202	B-VI-202	B-VI-202/ B-VI-201			B-VI-202/ B-VI-208	B-VI-202/ B-VI-208	
Friday	Course	UHV-II	CS LAB(B-I)/IC LAB(B-II)				AWP	LICA(T)-B-I /MPMC(T)-B-II	LIBRARY
	Faculty	VR	MR,VVR,DSR/SPK, DSK,PR				VVR	SPK/GRR	
	Venue	B-VI-202	B-VI-106/B-I-202				B-VI-202	B-VI-202/ B-VI-206	
Saturday	Course	SEMINAR							
	Faculty								
	Venue								

T: Tutorial, B-I: Batch-I, B-II: Batch-II

Course Title	Course Code & Course Name	Course Faculty
UHV-II	U18MH501: Universal Human Values-II	Dr.V.Raju (VR)
AIML	U18EC502A: Artificial Intelligence and Machine Learning with Python	D.Santosh Kumar (DSK)
CS	U18EC503: Communication Systems	Dr.M.Raju (MR)
AWP	U18EC504: Antennas and Wave Propagation	Dr.V.Venkateswar Reddy (VVR)
LICA	U18EC505: Linear Integrated Circuits and Applications	S.Pradeep Kumar (SPK)
MPMC	U18EC506: Microprocessors and Microcontrollers	Dr.G.Raghotham Reddy (GRR)
CS LAB	U18EC507: Communication Systems Laboratory	Dr.M.Raju (MR)/ Dr.V.Venkateswar Reddy (VVR), D.Srinivasa Rao
IC LAB	U18EC508: IC Applications Laboratory	S.Pradeep Kumar (SPK)/ D.Santosh Kumar (DSK),P.Yugander(PY),P.Raviteja(PR)
MPMC LAB	U18EC509: Microprocessors and Microcontrollers Laboratory	B.Komuraiah/S.Shiva Priyanka(SSP), P.Raviteja(PR)
SEMINAR	U18EC510: Seminar	

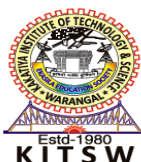
Sd/-
Faculty I/c,
Dept.Time Tables

Sd/-
Faculty I/c,
Overall Time Tables

Sd/-
Head of the
Department

Sd/-
Dean, AA

Sd/-
Principal



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ ప్రైవేట్ టెక్నాలజీస్ & సైన్స్, వరంగల్ - 506 015 తెలంగాణ, భారత్
కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - 506 015 తెలంగాణ, భారత్

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

website: www.kitsw.ac.in

E-mail: principal@kitsw.ac.in

☎ : +91 9392055211, +91 7382564888

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2021-22

V SEMESTER of B. Tech ECE (III)

Class Teacher: A.Srinivas

w.e.f: 13.09.2021

Day/Time		09.40-10.30	10.30-11.20	11.20-12.10	12.10-13.0		14.00 -14.50	14.50-15.40	15.40-16.30
Monday	Course	AIML	MPMC	UHV-II	LICA	L U N C H B R E A K	MPMC LAB(B-I)/CS LAB(B-II)		
	Faculty	AZS	SYZ	SPK	ES		SYZ,AM/KS,BNK		
	Venue	B-VI-203	B-VI-203	B-VI-203	B-VI-203		B-I-204/B-VI-106		
Tuesday	Course	CS	LICA	UHV-II	LIBRARY		AWP	AIML(T)-B-I /CS(T)-B-II	LIBRARY
	Faculty	KS	ES	SPK			MC	AZS/KS	
	Venue	B-VI-203	B-VI-203	B-VI-203			B-VI-203	B-VI-203/ B-VI-207	
Wednesday	Course	LICA(T)-B-I /MPMC(T)-B-II	IC LAB(B-I)/MPMC LAB(B-II)				CS	MPMC	AWP
	Faculty	ES/SYZ	ES,VK,AT/SYZ,AM				KS	SYZ	MC
	Venue	B-VI-203/ B-VI-208	B-I-202/B-I-204				B-VI-203	B-VI-203	B-VI-203
Thursday	Course	MPMC(T)-B-I /AIML(T)-B-II	AIML	CS(T)-B-I /AWP(T)-B-II	ECEA		CS LAB(B-I)/IC LAB(B-II)		
	Faculty	SYZ/AZS	AZS	KS/MC			KS,BNK/ES,VK		
	Venue	B-VI-203/ B-VI-201	B-VI-203	B-VI-203/ B-I-001A			B-VI-106/B-I-202		
Friday	Course	AIML	AWP	CS	LICA		AWP(T)-B-I /LICA(T)-B-II	MPMC	LIBRARY
	Faculty	AZS	MC	KS	ES		SPK/GRR	SYZ	
	Venue	B-VI-203	B-VI-203	B-VI-203	B-VI-203		B-VI-203/ B-VI-204	B-VI-203	
Saturday	Course	SEMINAR							
	Faculty								
	Venue								

T: Tutorial, B-I: Batch-I, B-II: Batch-II

Course Title	Course Code & Course Name	Course Faculty
UHV-II	U18MH501: Universal Human Values-II	S.Pradeep Kumar (SPK)
AIML	U18EC502A: Artificial Intelligence and Machine Learning with Python	A.Srinivas (AZS)
CS	U18EC503: Communication Systems	Dr.K.Sowjanya (KS)
AWP	U18EC504: Antennas and Wave Propagation	Dr.M.Chandrashekar (MC)
LICA	U18EC505: Linear Integrated Circuits and Applications	E.Suresh (ES)
MPMC	U18EC506: Microprocessors and Microcontrollers	Syed Zaheeruddin (SYZ)
CS LAB	U18EC507: Communication Systems Laboratory	Dr.K.Sowjanya (KS) /B.Neerajakshi(BNK)
IC LAB	U18EC508: IC Applications Laboratory	E.Suresh (ES)/Dr.V.Kishore(VK),Ayesha Tabassum(AT)
MPMC LAB	U18EC509: Microprocessors and Microcontrollers Laboratory	Syed Zaheeruddin (SYZ)/ Abdul Muqeem (AM)
SEMINAR	U18EC510: Seminar	

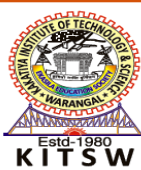
Sd/-
Faculty I/c,
Dept.Time Tables

Sd/-
Faculty I/c,
Overall Time Tables

Sd/-
Head of the
Department

Sd/-
Dean, AA

Sd/-
Principal



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ ప్రేక్షాగిక్రీ ఁవ్ విజ్ఞాన సంస్థాన, వరంగల్ - 506 015 తెలంగాణ, భారత

కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - 506 015 తెలంగాణ, భారతదేశము

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

website: www.kitsw.ac.in

E-mail: principal@kitsw.ac.in

☎ : +91 9392055211, +91 7382564888

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2021-22

VII SEMESTER of B. Tech ECE (I)

ClassTeacher: B. Komuraiah

w.e.f: 20.09.2021

Day/Time		09.40-10.30	10.30-11.20	11.20-12.10	12.10-13.0	L U N C H B R E A K	14.00 -14.50	14.50-15.40	15.40-16.30
Monday	Course	WCN	RTES	PM	LIBRARY		WCN LAB(B-I)/VLSI LAB(B-II)		
	Faculty	BD	SSP	AP			BD,SSP/AP,VSR		
	Venue	B-VI-204	B-VI-204	B-VI-204			B-I-205/B-VI-208		
Tuesday	Course	ESDM(T)-B-I /RTES(T)-B-II	PM	WCN	LIBRARY		WCN LAB(B-II)/VLSI LAB(B-I)		
	Faculty	BK/SSP	AP	BD			BD,SSP/AP,VSR		
	Venue	B-VI-204/ B-VI-206	B-VI-204	B-VI-204			B-I-205/B-VI-208		
Wednesday	Course	ESDM	RTES	ESDM	LIBRARY		WCN(T)-B-I /PM(T)-B-II	PM(T)-B-I /WCN(T)-B-II	LIBRARY
	Faculty	BK	SSP	BK			BD/AP	AP/BD	
	Venue	B-VI-204	B-VI-204	B-VI-204			B-VI-204/ B-VI-208	B-VI-204/ B-VI-208	
Thursday	Course	RTES	PM	ESDM	ECEA		WCN	RTES(T)-B-I /ESDM(T)-B-II	LIBRARY
	Faculty	SSP	AP	BK			BD	SP/BK	
	Venue	B-VI-204	B-VI-204	B-VI-204			B-VI-204	B-VI-204/ B-I-001A	
Friday	Course	PROJ					IE		
	Faculty								
	Venue								
Saturday	Course	PROJ							
	Faculty								
	Venue								

T: Tutorial, B-I: Batch-I, B-II: Batch-II

Course Title	Course Code & Course Name	Course Faculty
PM	U18OE701B: Project Management	A.Pavan (AP)
RTES	U18EC702B: Real Time Embedded Systems	S.Shiva Priyanka (SSP)
ESDM	U18EC703A: Electronic System Design and Manufacturing	B.Komuraiah (BK)
WCN	U18EC704: Wireless Communication and Networks	Dr.B.Dhanalakshmi (BD)
WCN LAB	U18EC705: Wireless Communication and Networks Lab	Dr.B.Dhanalakshmi (BD)/S.Shiva Priyanka(SSP)
VLSI LAB	U18EC706: VLSI Laboratory	A.Pavan (AP)/V.Shoban Reddy(VSR)
PROJ	U18EC707: Major Project Phase - I	R.Srikanth (RS)
IE	U18EC708: Internship Evaluation	

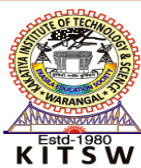
Sd/-
Faculty I/c,
Dept.Time Tables

Sd/-
Faculty I/c,
Overall Time Tables

Sd/-
Head of the
Department

Sd/-
Dean, AA

Sd/-
Principal



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ ప్రేక్షాగిరికి అం విజ్ఞాన సంస్థాన, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారత

కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారతదేశము

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

website: www.kitsw.ac.in

E-mail: principal@kitsw.ac.in

☎ : +91 9392055211, +91 7382564888

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2021-22

VII SEMESTER of B. Tech ECE (II)

ClassTeacher: Dr.S.Umamaheshwar

w.e.f: 20.09.2021

Day/Time		09.40-10.30	10.30-11.20	11.20-12.10	12.10-13.0	L U N C H B R E A K	14.00 -14.50	14.50-15.40	15.40-16.30	
Monday	Course	WCN LAB(B-I)/VLSI LAB(B-II)			WCN		DM	DSE	DIP	
	Faculty	SU,RP/VR,PR			SU		DSR	PR	SPG	
	Venue	B-I-203/B-VI-208			B-VI-205		B-VI-205	B-VI-205	B-VI-205	
Tuesday	Course	WCN	DM	DSE	DIP		DSE	LIBRARY	LIBRARY	
	Faculty	SU	DSR	PR	SPG		PR			
	Venue	B-VI-205	B-VI-205	B-VI-205	B-VI-205		B-VI-205			
Wednesday	Course	DIP	WCN(T)-B-I /DM(T)-B-II	DSE(T)-B-I /DIP(T)-B-II	LIBRARY		DM	IE		
	Faculty	SPG	SU/DSR	PR/SPG			DSR			
	Venue	B-VI-205	B-VI-205/ B-VI-206	B-VI-205/ B-VI-206			B-VI-205			
Thursday	Course	WCN LAB(B-II)/VLSI LAB(B-I)			ECEA		WCN	DM(T)-B-I /WCN(T)-B-II	DIP(T)-B-I /DSE(T)-B-II	
	Faculty	SU,RP/VR,PR					SU	DSR/SU	SPG/PR	
	Venue	B-I-203/B-VI-208					B-VI-205	B-VI-205/ B-I-203	B-VI-205/ B-I-203	
Friday	Course	PROJ								
	Faculty									
	Venue									
Saturday	Course	PROJ								
	Faculty									
	Venue									

T: Tutorial, B-I: Batch-I, B-II: Batch-II

Course Title	Course Code & Course Name	Course Faculty
DM	U18OE701A: Disaster Management	D.Srinivasa Rao (DSR)
DSE	U18EC702A: Data Science and Engineering	P.Raviteja (PR)
DIP	U18EC703C: Digital Image Processing	S.P.Girija (SPG)
WCN	U18EC704: Wireless Communication and Networks	Dr.S.Umamaheshwar (SU)
WCN LAB	U18EC705: Wireless Communication and Networks Lab	Dr.S.Umamaheshwar (SU)/Dr.R.Prashanth (RP)
VLSI LAB	U18EC706: VLSI Laboratory	Dr.V.Raju(VR)/P.Raviteja(PR)
PROJ	U18EC707: Major Project Phase - I	Dr.R.Prashanth (RP)
IE	U18EC708: Internship Evaluation	

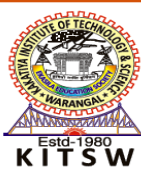
Sd/-
Faculty I/c,
Dept.Time Tables

Sd/-
Faculty I/c,
Overall Time Tables

Sd/-
Head of the
Department

Sd/-
Dean, AA

Sd/-
Principal



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ ప్రేక్షాగిరికి అంబ విజ్ఞాన సంస్థాన, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారత

కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారతదేశము

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

website: www.kitsw.ac.in

E-mail: principal@kitsw.ac.in

☎ : +91 9392055211, +91 7382564888

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2021-22

VII SEMESTER of B. Tech ECE(III)

Class Teacher: Dr.M.Chandrashekar

w.e.f: 20.09.2021

Day/Time		09.40-10.30	10.30-11.20	11.20-12.10	12.10-13.0		14.00 -14.50	14.50-15.40	15.40-16.30	
Monday	Course	WCN	RTES	DM(T)-B-I/ VLSIP(T)-B-II	LIBRARY	L U N C H B R E A K	VLSIP	RTES(T)-B-I/ WCN(T)-B-II	LIBRARY	
	Faculty	MC	BK	AM/CHPK			CHPK	BK/MC		
	Venue	B-VI-206	B-VI-206	B-VI-206/ B-VI-201			B-VI-206	B-VI-206/ B-VI-203		
Tuesday	Course	WCN LAB(B-I)/VLSI LAB(B-II)			DM		RTES	VLSIP(T) -B-I/ - DM(T)B-II	WCN(T) -B-I/ RTES(T)-B-II	
	Faculty	MC,DSK/CHPK,PC			AM		BK	CHPK/AM	MC/BK	
	Venue	B-I-203/B-VI-208			B-VI-206		B-VI-206	B-VI-206/ B-VI-205	B-VI-206/ B-VI-205	
Wednesday	Course	WCN LAB(B-II)/VLSI LAB(B-I)			VLSIP		WCN	DM	LIBRARY	
	Faculty	MC,DSK/ CHPK,PY			CHPK		MC	AM		
	Venue	B-I-205/B-VI-208			B-VI-206		B-VI-206	B-VI-206		
Thursday	Course	RTES	DM	VLSIP	ECEA		WCN	IE		
	Faculty	BK	AM	CHPK			MC			
	Venue	B-VI-206	B-VI-206	B-VI-206			B-VI-206			
Friday	Course	PROJ								
	Faculty									
	Venue									
Saturday	Course	PROJ								
	Faculty									
	Venue									

T: Tutorial, B-I: Batch-I, B-II: Batch-II

Course Title	Course Code & Course Name	Course Faculty
DM	U18OE701A: Disaster Management	Md.Abdul Muqeem (AM)
RTES	U18EC702B: Real Time Embedded Systems	B.Komuraiah (BK)
VLSIP	U18EC703B: VLSI Physical Design	Ch.Pavan Kumar (CHPK)
WCN	U18EC704: Wireless Communication and Networks	Dr.M.Chandrashekar (MC)
WCN LAB	U18EC705: Wireless Communication and Networks Lab	Dr.M.Chandrashekar (MC)/ D.Santosh Kumar(DSK)
VLSI LAB	U18EC706: VLSI Laboratory	Ch.Pavan Kumar (CHPK)/ P.Yugander(PY), P.Chiranjeevi(PC)
PROJ	U18EC707: Major Project Phase - I	Dr.Ch.Sridevi(CHS)
IE	U18EC708: Internship Evaluation	

Sd/-
Faculty I/c,
Dept.Time Tables

Sd/-
Faculty I/c,
Overall Time Tables

Sd/-
Head of the
Department

Sd/-
Dean, AA

Sd/-
Principal