


FACULTY PROFILE

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Name of the Faculty	Designation/ Affiliation	Address/Contact	Email (Institute) @kitsw.ac.in	
Dr. Dhanalaxmi Banavath	Assistant Professor	H.No. 35-6-142, Saraswathi Nagar, Gopalpur, Hanamkonda, Warangal	dhanalaxminov20@gmail.com	

EDUCATION

Degree	Details (specialization)	Institute/University	Month , Year completed
Ph. D.	ECE	KU College of engineering and Technology, Kakatiya University, Warangal	May-2019
M.Tech.	ECE (VLSI System Design)	Aurora engineering College Bhongiri/JNTUH	Jan-2012
B.Tech.	ECE	Swami Rama Nanda Tirtha Institute of Science and Technology, Nalgonda/JNTUH	June-2006

ACADEMIC AFFILIATION

Details	From (Month, Year)	To (Month, Year)	Name of the Organization
Assistant Professor	12 th July-2019	<i>Till date</i>	Kakatiya Institute of Technology & Science, Warangal
Assistant Professor	12 th July-2010	30 th May-2015	Prasad Engineering College, Jangaon, Warangal
IT Associate	21 st August- 2006	15 th June-2007	Electronic Governance IIIT Campus, Gachibowli, Hyderabad.

RESEARCH INTERESTS

Cryptography, Security of Medical images and Enhancement of Bio-medical sensor to diagnosis different diseases.

PUBLICATIONS

Publications in Refereed Journals

Sl. No.	Title of the paper	Name of the Journal	Vol. No.	Issue No.	Month & Year of publication
1	A new self-adaptive approach for medical imaging security	International Journal of Computer Science and Information Security. THOMSON REUTER SCOPUS, SCI	16	6	June 2018 ISSN 1947-5500
2	A new image encryption algorithm based on logistic chaotic map	International Journal of Advanced Research in Electronics and Communication Engineering. UGC APPROVED	6	11	November 2017, pp. 1248-1260
3	Implementation of an Efficient Hardware Architecture for Multimedia Encryption and Authentication Using the Discrete Wavelet Transform	London Journal of Research in Computer Science and Technology. SCOPUS	17	2	January 2017, pp. 20-26.
4	Multimedia Cryptography – A Review	IEEE XPLORE			
5	A novel image self-adaptive encryption algorithm based on composite chaotic system	International Journal of Electronics Engineering UGC APPROVED	9	1	Jan-June 2017, pp. 160-164

Conferences attended for paper presentation

S.No.	Name of the topic	Name of the conference	Month & Year
1	Multimedia crypto security-introductory review	International conference on science and technology. Future challenges and solutions, University of Mysore	August 8-9, 2016.
2	Multimedia Cryptography – A Review	IEEE international conference, Thandalam, Chennai, Tamilnadu	September, pp.136-

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Workshops/Conference Attended:

- Attended six day faculty development programme on “Computer Vision and Image Processing- Research Focus” at National Institute of Technology, Warangal, and 11th – 16th June, 2019.
- Attended three days “Orientation Program on MATLAB Programming” at MSME-TOOL ROOM, Central Institute of Tool Design, Hyderabad. 10th-12th May 2018.
- Attended six day faculty development programme on “Cyber Security” at National Institute of Technology, Warangal, and 19th – 24th December, 2017.
- Attended six day faculty development programme on “Big Image Data Processing Using Machine Learning Algorithms” at National Institute of Technology, Warangal, and 11th – 16th December, 2017.
- Paper presented in IEEE International Conference on Power, Control, Signal and Instrumentation Engineering (ICPCSI)-2017. Thandalam, Chennai, Tamilnadu, 21st & 22nd September, 2017.
- Attended ten days program on “Foundations of Cryptography” at National Institute of Technology, Warangal. 19th – 30th December, 2016.
- Attended One week National Seminar on “Research Methodology and computational techniques (RMCT-2016)” at Kakatiya Institute of Technology, Warangal, 27th June to 1st July, 2016.
- Attended one day IEEE Prime Asia 2015, Asia-pacific conference on postgraduate research in microelectronics and electronics, conference course work at Vasavi College of Engineering & Technology Hyderabad, 27th-29th November, 2015.

COURSES TAUGHT			
Undergraduate Level			
Courses Taught		Currently Teaching	
Course	Semester, Year, Branch	Course	Semester, Year, Branch
Electronic Devices & Circuits,	III,II,ECE	Electronic Devices & Circuits,	III,II,EEE
Digital Signal Processing,	V,III,ECE		
Digital Image Processing	IV,II,ECE		
Switching theory and logic design	III,II,ECE		
Postgraduate Level			
<i>Guiding PG Projects for M.Tech. (VLSI&ES) students</i>			

PROFESSIONAL AFFILIATION

S.No	Details
1	<i>IETE, New Delhi.</i>
2	<i>ISI Cryptology and Security Kolkata.</i>