




Faculty Profile

Name of the Faculty	Designation/ Affiliation	Address/Contact	Email (Institute) @kitsw.ac.in	
Santhosh Kumar Dontha	Assistant Professor	<i>Office:</i> Block-VI, Dept. of ECE <i>Phone:</i> 0870 – 2564888 <i>Mobile:</i>	santhoshdontha@gmail.com	

EDUCATION

Degree	Details (specialization)	Institute/University	Month , Year completed
B.Tech.	Electronics and Communication Engg.	Balaji Institute of Technology and Science, Warangal	June, 2006
M.Tech.	Electronics and Communication Engg. & VLSI and Embedded Systems	Kakatiya Institute of Technology and Science, Warangal	July, 2009

ACADEMIC AFFILIATION

Details	From (Month, Year)	To (Month, Year)	Name of the Organization
Assistant Professor	10.07.2019	Till date	Kakatiya Institute of Technology & Science, Warangal.
Assistant Professor	14.06.2010	08.07.2019	Talla Padmavathi College of Engineering, Warangal
Assistant Professor	25.09.2006	25.05.2010	Balaji Institute of Technology and Science, Warangal

RESEARCH INTERESTS

Digital VLSI, Deep Learning.

Ph.D Title:

FDPS / STTPS / WORKSHOPS / SHORT-TERM COURSES ATTENDED

S.No	Academic Year	Programme	Duration	Organized by
1.	2018-19	VLSI Design: Bridging Concepts to Practice	1 Week	Kakatiya Institute of Technology & Science, Warangal.
2.	2017-18	Trends in Reconfigurable (FPGA) SoC Design	1 Week	National Institute of Technology, Warangal.
3.	2017-18	Advanced embedded system design on ZYNQ ultra scale using vivado	1 Week	JNTUH - College of Engg., Hyderabad
4.	2015-16	Aspects of IC Design	10 Days	National Institute of Technology, Warangal.
5.	2013-14	Analog Electronics	2 Week	National Institute of Technology, Warangal.
6.	2015-16	Digital VLSI System Design using VERILOG HDL	1 Week	C-DAC Hyderabad
7.	2009-10	Signals, Systems & Transforms	2 Week	National Institute of Technology, Warangal.

COURSES TAUGHT

Undergraduate Level			
Courses Taught		Currently Teaching	
Course	Semester, Year, Branch	Course	Semester, Year, Branch
Signals & Systems	<i>I Sem, 2nd ECE</i>	<i>Satellite Communications</i>	<i>VII, 4th ECE</i>
Network Analysis	<i>I Sem, 2nd ECE</i>		
Electronic Devices and Circuits	<i>I Sem, 2nd EEE</i>		
Analog Electronics	<i>II Sem, 2nd ECE</i>		
Switching Theory and Logic Design	<i>II Sem, 2nd ECE</i>		
Linear and Digital IC Applications	<i>I Sem, 3rd ECE</i>		
VLSI Design	<i>I Sem, 4th ECE</i>		
Digital Design Through Verilog HDL	<i>II Sem, 4th ECE</i>		
Analog Communications	<i>I sem 3rd ECE</i>		
Postgraduate Level			
<i>Guiding PG Projects for M.Tech. (VLSI&ES) students</i>			

PROFESSIONAL AFFILIATION

S.No	Details
1	<i>Member in Institution of Electronics and Telecommunication Engineers</i>

OTHER ACTIVITIES

S.No	Details
1	Organized 2 day work shop on "MULTISIM" for the 2 year ECE students in 2017 as a resource person at Talla Padmavathi College of Engineering.
2	Trained students on Verilog Programming and XILINX tool in 2014, 2016 & 2018 years.
3	Organized National Level Students Technical Symposium PRVACHAN-11.
4	Guided B.tech and M.Tech projects as a Project Coordinator.