P14PE101 MACHINE MODELLING AND ANALYSIS

Class: M.Tech. I Semester Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : | 40 marks |
| End Semester Exam | : | 60 marks |

Course Learning Objectives:
- To understand the concept of 2-axis representation of an Electrical machine.
- To know the concepts of representing transfer function model of a DC machine.
- To understand the importance of 3-phase to 2-phase conversion.
- To know the representation of 3-phase induction motor in various reference frames.
- To know the modeling of 3-phase synch. Motor in 2-axis representation.

UNIT – I (12)

UNIT – II (12)
Linear transformation - Phase transformation (a,b,c to α, β, ω) - Active transformation(α, β, ω to d, q ). Circuit model of a 3 phase Induction motor - Linear transformation - Phase Transformation - Transformation to a Reference frame - Two axis models for Induction motor.

UNIT – III (12)
Voltage and current Equations in stator reference frame - Equation in Rotor reference frame - Equations in a synchronously rotating frame - Torque equation-Equations in state-space form.

UNIT – IV (12)

Text Books:
1. Vedam Subramanyam, "Thyristor control of Electric Drives".
2. Paul C.Krause, Oleg wasynezuk, Scott D.Sudhoff, “Analysis of Electric Machinery and Drive Systems”
Course Learning Outcomes:
At the end of the course the student will be able to:

- Develop models for linear and nonlinear magnetic circuits
- Determine the developed torque in an electrical machines using the concepts of filed energy and co-energy and determine the dynamic model of a DC Machine
- Determine the dynamic model of an induction machine based on the dq0 transformation and determine instantaneous torque developed in an induction machine—which leads to advanced control strategies such as vector control and direct torque control.
- Determine the torque developed in a salient pole synchronous machine using the park’s transformation and identify contribution of saliency torque-damping torque and excitation torque.
P14PE102 ANALYSES OF POWER ELECTRONIC CONVERTERS

Class: M.Tech. I Semester  Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : 40 marks |
| End Semester Exam | : 60 marks |

Course Learning Objectives:

- To analyze the performance of controlled AC-DC converters.
- To understand designing concepts of AC-AC voltage controllers and Cycloconverters.
- To learn the designing of DC-DC chopper circuits.
- To understand and analyze PWM techniques for Inverters.

UNIT – I (12)

Single Phase AC Voltage Controllers: Single phase AC voltage controllers with Resistive, Resistive-inductive and Resistive-inductive-induced e.m.f. loads.

Three Phase AC Voltage Controllers: Three phase AC voltage controllers - Analysis of controllers with star and delta Connected Resistive, Resistive-inductive loads - Effects of source and load inductances - Synchronous tap changers-. ac voltage controllers with PW Control ,Applications - numerical problems.


UNIT – II (12)


UNIT – III (12)

UNIT - IV (12)

Pulse Width Modulated Inverters (single phase):

Pulse Width Modulated Inverters (three phase).
Three phase inverters - analysis of 180 degree condition for output voltage And current with resistive, inductive loads - analysis of 120 degree Conduction - voltage control of three phase inverters - sinusoidal PWM - Third Harmonic PWM - 60 degree PWM - space vector modulation - Comparison of PWM techniques - harmonic reductions - Current Source Inverter - variable d.c. link inverter - boost inverter - buck and boost inverter - inverter circuit design - advantages - applications - numerical problems.

Multilevel inverters

Text Books:

Course Learning Outcomes:
At the end of the course the student will be able to:
- Select an appropriate power semiconductor device and design a power converter for the required application
- Determine the power circuit configuration needed to fulfill the required power conversion with applicable constraints.
- Design the control circuit and the power circuit for a given power converter.
- Determine the drive circuit requirements in terms of electrical isolation and the requirement of bipolar drive and ease of control.
P14PE103 MODERN CONTROL THEORY

Class: M.Tech. I Semester                  Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : | 40 marks |
| End Semester Exam | : | 60 marks |

Course Learning Objectives:

- To know the basic concepts of matrices, Eigen values and Eigen vectors.
- To know the modeling of systems by using state space Analysis
- To know the design of controllers for several classes of plants.
- To know the Harmonic Analysis and Stability of Non-Linear Systems
- To know the control problems such as dead bent control, external Disturbances and sensitivity problems in optimal linear regulators.

UNIT – I (12)


STATE VARIABLE ANALYSIS: Linear Continuous time models for Physical systems– Existence and Uniqueness of Solutions to Continuous-Time State Equations – Solutions of Linear Time Invariant Continuous-Time State Equations – State transition matrix and it’s properties.

CONTROLLABILITY AND OBSERVABILITY


UNIT – II (12)

UNIT – III (12)
STABILITY ANALYSIS: Stability in the sense of Lyapunov, Lyapunov’s stability and
Lypanov’s instability theorems - Stability Analysis of the Linear continuous time invariant
systems by Lyapunov second method – Generation of Lyapunov functions – Variable
gradient method – Krasooviski’s method.
STATE FEEDBACK CONTROLLEbRS AND OBSERVERS
State feedback controller design through Pole Assignment – State observers: Full order
and Reduced order

UNIT – IV (12)
Introduction to optimal control - Formulation of optimal control problems – calculus of
variations – fundamental concepts, functionals, variation of functionals – fundamental
theorem of theorem of Calculus of variations – boundary conditions – constrained
minimization – formulation using Hamiltonian method – Linear Quadratic regulator

Text books:

Reference Books:

Course Learning Outcomes:
At the end of the course the student will be able to:
- Develop mathematical models of physical systems.
- Design optimal controllers for physical systems including power electronic and power
  systems.
- Analyze the issues related to the stability of automatic control systems.
- Design complex nonlinear systems by linearizing them
P14PE104 POWER ELECTRONIC CONTROL OF DC DRIVES

Class: M.Tech. I Semester  Branch: Power Electronics

Teaching Scheme:                  Examination Scheme:

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Continuous Internal Evaluation : 40 marks
End Semester Exam : 60 marks

Course Learning Objectives:

- To Design motor starting, braking and reversing electrical circuits.
- To Control Speed of the DC motor by using different controllers.
- To Design and modeling of current and speed controllers for DC motors.
- To Determine drive system stability by calculating different system parameters.
- To Calculate harmonics and their associated problems.

UNIT – I (12)

Controlled Bridge Rectifier (1-Φ) with DC Motor Load: Separately exited DC motors with rectified single-phase supply – single phase semi converter and single phase full converter for continuous and discontinuous modes of operation – power and power factor.

Controlled Bridge Rectifier (3-Φ) with DC Motor Load: Three-phase semi converter and three phase full converter for continuous and discontinuous modes of operation – power and power factor – Addition of freewheeling diode.

Three phase naturally commutated bridge circuit as a rectifier or as an inverter: Three phase controlled bridge rectifier with passive load impedance, resistive load and ideal supply – Highly inductive load and ideal supply for load side and supply side quantities.

UNIT – II (12)


Current and Speed Controlled DC Motor drives:
UNIT – III (12)

Chopper controlled DC Motor drives
Principles of operation of the chopper - four-quadrant chopper circuit - chopper for inversion - Chopper with other power devices - model of the chopper - input to the chopper steady state analysis of chopper controlled DC motor drives - rating of the devices - Pulsating torque.

UNIT – IV (12)

Closed loop operation of DC Motor drives
Speed controlled drive system - current control loop - pulse width modulated current controller - hysteresis current controller - modeling of current controller - design of current controller.

Simulation of DC motor drives: Dynamic simulations of the speed controlled DC motor drives - Speed feedback speed controller - command current generator - current controller.

References Books:

Course Learning Outcomes:
At the end of the course the student will be able to:
- Design controllers for closed-loop operation of a separately excited DC motor drive.
- Control Speed of the DC motor by using different controllers.
- Design and modeling of current and speed controllers for DC motors
P14PE105 A HIGH VOLTAGE DC TRANSMISSION
(Elective-I)

Class: M.Tech. I Semester    Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : | 40 marks |
| End Semester Exam | : | 60 marks |

Course Learning Objectives:
1. To learn the importance of HVDC transmission.
2. To analyze HVDC converters
3. To know the faults and protections required in HVDC system
4. To get idea of Harmonics and Filters
5. To know the concepts of multi terminal DC links.

**UNIT – I (12)**

H.V.D.C. Transmission: General considerations, Power Handling Capabilities of HVDC Lines, Basic Conversion principles, static converter configuration. Static Power Converters: 3-pulse, 6-pulse and 12-pulse converters, converter station and Terminal equipment, commutation process, Rectifier and inverter operation, equivalent circuit for converter -special features of converter transformers. Harmonics in HVDC Systems, Harmonic elimination, AC and DC filters.

**UNIT – II (12)**

Control of HVDC Converters and systems: constant current, constant extinction angle and constant Ignition angle control- Individual phase control and equidistant firing angle control, DC power flow control. Interaction between MV AC and DC systems - Voltage interaction, Harmonic instability problems and DC power modulation.

**UNIT – III (12)**

Multi-terminal DC links and systems; series, parallel and series parallel systems, their operation and control. Transient over voltages in HVDC systems: Over voltages due to disturbances on DC side, over voltages due to DC and AC side line faults.

**UNIT – IV (12)**

Converter faults and protection in HVDC Systems: Converter faults, over current protection - valve group, and DC line protection. Over voltage protection of converters, surge arresters.
Reference Books:

Course Learning Outcomes:
At the end of the course the student will be able to:
- Compare the HVDC Transmission and EHVAC transmission
- Identify and analyze converter configurations used in HVDC and list the performance metrics.
- Understand controllers for controlling the power flow through a dc link.
- Understand the role of impedance control, phase angle control and voltage control in controlling real and reactive power in transmission systems.
P14PE105 B DESIGN OF DIGITAL SYSTEMS  
(Elective-I)  
Class: M.Tech. I Semester  
Branch: Power Electronics

Teaching Scheme:
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Course Learning Objectives:
- To learn Simulation of complex digital high-and design systems and FSMs with level software tools such as VHDL
- To learn the synthesis of digital systems and select devices and technologies
- To learn fast hard-wired controllers to suit the needs of high-end power electronics applications such as Multi-Level inverter driven AC drives and FACTS devices

UNIT – I (12)

Principles of Sequential logic design: Concept of FSM – Metastability

UNIT – II (12)

State machine structures: Moore machine - Mealy machine, Analysis of state machine with D and J-K Flip-flops

UNIT – III (12)

Design of Finite State Machines: Clocked synchronous state machine design, Designing state machine using state diagrams, State machine synthesis using transition list, Clock skew- Overview of PLDs, CPLDs and FPGAs

UNIT – IV (12)

RT level combinational circuit, Regular sequential circuit Design examples with VHDL

Reference Books:

Course Learning Outcomes:
At the end of the course the student will be able to:
1. Simulate complex digital high-and design systems and FSMs with level software tools such as VHDL
2. Synthesize digital systems and select devices and technologies
3. Implement fast hard-wired controllers to suit the needs of high-end power electronics applications such as Multi-Level inverter driven AC drives and FACTS devices
P14PE105 C OPTIMIZATION TECHNIQUES
(Elective-I)

Class: M.Tech. I Semester Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

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<td>End Semester Exam</td>
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Course Learning Objectives:

- To learn the importance and value of optimization techniques in solving practical Problems in industry
- To learn optimization models and apply them to real life problems
- To learn design of new models to improve decision making and develop critical thinking and objective analysis of decision problems.

UNIT – I (12)

Linear Programming:

UNIT – II (12)

Nonlinear Programming:
Classical optimization methods - equality and inequality constraints - Lagrange multipliers and Kuhn-Tucker conditions - quadratic forms - quadratic programming problem and Wolfe’s method.

UNIT – III (12)

Search Methods:

UNIT – IV (12)

Dynamic Programming:
Principle of optimality - recursive relations - solution of LPP - simple examples.

Integer Linear Programming:
Gomory’s cutting plane method - Branch and bound algorithm - Knapsack problem - linear 0-1 problem.
Reference Books:
1. J.C. Pant, “Introduction to Optimization”, Jain Brothers, 2004

Course Learning Outcomes:
At the end of the course the student will be able to:

- Recognize the importance and value of optimization techniques in solving practical problems in industry
- Understand optimization models and apply them to real life problems
- Design new models to improve decision making and develop critical thinking and objective analysis of decision problems.
P14PE105 D  MODELING AND SIMULATION OF POWER ELECTRONIC SYSTEMS  
(Elective-I)  

Class: M.Tech. I Semester
Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : | 40 marks |
| End Semester Exam | : | 60 marks |

Course Learning Objectives:
- To understand the background processes related to the numerical solution used.
- To choose the numerical solver to be used for a given type of analysis.
- To understand the reason for convergence problems occurring during simulation and to avoid them.
- To simulate the behavior of Power Converters, DC and AC drives

UNIT-I (12)

Introduction:
Challenges in computer simulation - Simulation process - mechanics of simulation - Solution techniques for time domain analysis - Equation solvers - circuit-oriented simulators

UNIT-II (12)

Simulation Of Power Electronic Converters:
State-space representation of power electronic converters (with buck converter as a representative example) - Trapezoidal integration - M & N method for simulating power electronic converters (with buck converter as a representative example) - Introduction to MATLAB and Simulink - Simulation of rectifiers - choppers and inverter circuits along with PWM techniques

UNIT-III (12)

Simulation of Electric Drives:
Modeling of power electronic converters with transportation delay - Concept of control gain - linearization of rectifiers with inverse cosine control - State space model of 3-Ph IM - Principle of Vector control - Modeling and simulation of Vector controlled 3-Ph IM with a 3-level inverter drive

UNIT-IV (12)

Modeling - Simulation Of Switching Converters With State Space Averaging:
State Space Averaging Technique- Modeling AND linearization of converter transfer functions - Simulation and Design of power electronic converters using State-space averaged models
Reference Books:

Course Learning Outcomes:
At the end of the course the student will be able to:
- Understand the background processes related to the numerical solution used.
- Choose the numerical solver to be used for a given type of analysis.
- Understand the reason for convergence problems occurring during simulation and to avoid them.
- Simulate the behavior of Power Converters, DC and AC drives
P14PE106 A ALTERNATIVE SOURCES OF ELECTRICAL ENERGY
(Elective-II)

Class: M.Tech. I Semester Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : | 40 marks |
| End Semester Exam | : | 60 marks |

Course Learning Objectives:

- To know the characteristics and performance of solar cells.
- To learn the principles of wind energy conversion
- To learn the concept of Distributed Generation, Demand & Supply Side Management.
- To learn the Energy Storage Systems

UNIT – I (12)
Renewable Sources of Energy; Grid-Supplied Electricity; Distributed Generation; Renewable Energy Economics - Calculation of Electricity Generation Costs; Demand-Side Management Options; Supply-Side Management Options; Electricity Act 2003, Energy conservation & integrated policy

UNIT – II (12)
Wind Power Plants:
Appropriate Location; Evaluation of Wind Intensity; Topography; Purpose of the Energy Generated - General Classification of Wind Turbines; Rotor Turbines; Multiple-Blade Turbines; Drag Turbines; Lifting Turbines - Generators and Speed Control Used in Wind Power Energy; Analysis of Small Generating Systems

UNIT – III (12)
Photovoltaic Power Plants:
Solar Energy; Generation of Electricity by Photovoltaic Effect; Dependence of a PV Cell Characteristic on Temperature; Solar Cell Output Characteristics - Equivalent Models and Parameters for Photovoltaic Panels; Photovoltaic Systems - Applications of Photovoltaic Solar Energy; Economical Analysis of Solar Energy

UNIT – IV (12)
Energy Storage Systems:
Energy Storage Parameters; Lead-Acid Batteries; Ultracapacitors; Flywheels - Superconducting Magnetic Storage System; Pumped Hydroelectric Energy Storage; Compressed Air Energy Storage - Storage Heat; Energy Storage as an Economic Resource
Text Books:

5. G.D.Rai, “Non conventional Sources of Energy” Khanna publishers New delhi

Reference Books:


Course Learning Outcomes:
At the end of the course the student will be able to:

- Understand the characteristics and performance of solar cells.
- Understand the principles of wind energy conversion
- Understand the concept of Distributed Generation, Demand & Supply Sid Management
- Understand the Learn the Energy Storage Systems
P14PE106 B  DYNAMICS OF ELECTRICAL MACHINES  
(Elective-II) 
Class: M.Tech. I Semester                   Branch: Power Electronics

Teaching Scheme:                              Examination Scheme:
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Continuous Internal Evaluation : 40 marks
End Semester Exam : 60 marks

Course Learning Objectives:
- To learn various design and characteristics of electrical machines.
- To learn the electromechanical analogy of the electrical machines
- To learn the dynamics characteristics of DC machine with generalized machine theory.
- To know and study the operation and dynamics characteristics of the induction motor.
- To learn the dynamic, Transient and steady state characteristics of synchronous machines.

UNIT – I (12)

UNIT – II (12)

UNIT – III (12)

UNIT – IV (12)
Reference Books:

Course Learning Outcomes:
At the end of the course the student will be able to:
- Understand the various design and characteristics of electrical machines.
- Understand the electromechanical analogy of the electrical machines
- Understand the dynamics characteristics of DC machine with generalized machine theory
- Understand the operation and dynamics characteristics of the induction motor.
- Understand the dynamic, Transient and steady state characteristics of synchronous machines.
P14PE106 C  ELECTRO MAGNETIC INTERFERENCE AND COMPATIBILITY (Elective-II)

Class: M.Tech. I Semester  Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : 40 marks |
| End Semester Exam | : 60 marks |

Course Learning Objectives:

- To recognize the sources of conducted and radiated EMI in Power Electronic converters and consumer appliances and suggest remedial measures to mitigate the problems
- To assess the insertion loss and design EMI filters to reduce the loss
- To design EMI filters, common-mode and RC-snubber circuits chokes measures to keep the interference within tolerable limits

UNIT-I (12)
Introduction: EMC standardization and description, measuring instruments, conducted EMI references, EMI in power electronic equipment: EMI from power semiconductors circuits.

UNIT-II (12)
Noise suppression in relay systems: AC switching relays, shielded transformers, capacitor filters, EMI generation and reduction at source, influence of layout and control of parasites.

UNIT-III (12)
EMI filter elements: Capacitors, choke coils, resistors, EMI filter circuits.

UNIT-IV (12)
EMI filter design for insertion loss: Worst case insertion loss, design method for mismatched impedance condition and EMI filters with common mode choke-coils.

Text Books:

Course Learning Outcomes:
At the end of the course the student will be able to:
1. Recognize the sources of conducted and radiated EMI in Power Electronic converters and consumer appliances and suggest remedial measures to mitigate the problems
2. Assess the insertion loss and design EMI filters to reduce the loss
3. Design EMI filters, common-mode and RC-snubber circuits chokes measures to keep the interference within tolerable limits
P14PE106 D  RELIABILITY ENGINEERING
(Elective-II)
Class: M.Tech. I Semester Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : | 40 marks |
| End Semester Exam | : | 60 marks |

Course Learning Objectives:

- To learn about the element’s of probability theory
- To learn about the significance of reliability and hazard models.
- To learn about the reliability logic diagrams.
- To learn about the Discrete Markov chains and reliability evaluation of repairable systems.

UNIT – I (12)


UNIT – II (12)


UNIT – III (12)


UNIT – IV (12)

**Text Books:**

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<td>• <em>Understand the reliability logic diagrams.</em></td>
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<td>• <em>Understand the Discrete Markov chains and reliability evaluation of repairable systems.</em></td>
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Course Learning Objectives:

- To Design the control circuit and the power circuit for DC-DC converters
- To Verify the compliance of spectral performance of a Three-phase voltage source Inverter
- To Critically compare various options available for the drive circuit requirements
- To Recognize possible modes of failure of a circuit- troubleshoot and repair

List of Experiments:

1. Experimental study for characteristics of DC-DC Buck converter.
2. Experimental study for characteristics of DC-DC Boost converter.
3. Experimental study for characteristics of DC-DC Buck-Boost converter.
4. Experimental study for characteristics of single phase fully controlled Full-Bridge converter.
5. Experimental study for characteristics of single phase semi controlled Full-Bridge converter.
6. Experimental study for single phase AC voltage controller using TRIAC
7. Experimental study for single phase Inverter.
8. Experimental study for three phase Inverter.
9. Experimental study for characteristics of three phase fully controlled Full-Bridge converter.
10. Experimental study for characteristics of three phase semi controlled Full-Bridge converter

Course Learning Outcomes:

The students will be able to:

- Design the control circuit and the power circuit for DC-DC converters
- Verify the compliance of spectral performance of a Three-phase voltage source Inverter
- Understand the critically compare various options available for the drive circuit requirements
- Recognize possible modes of failure of a circuit- troubleshoot and repair
### Course Learning Objectives:

- To perform simulations study of stability of system.
- To write a program for steady state and transient stability analysis of a power system.
- To perform simulation for load frequency control of a single-area and two area system.
- To perform simulation analysis of various power electronic converters with different loads.

### List of Experiments:

1. Write program and simulate dynamical system of following models:
   - a) I/O Model
   - b) State variable model
   Also identify time domain specifications of each.
2. Obtain frequency response of a given system by using various methods:
   - a) General method of finding the frequency domain specifications.
   - b) Polar plot
   - c) Bode plot
   Also obtain the Gain margin and Phase margin.
3. Determine stability of a given dynamical system using following methods:
   - a) Root locus
   - b) Bode plot
   - c) Nyquist plot
4. Design a compensator for a given systems for required specifications.
5. Design a PID controller.
6. PSPICE &MATLAB Simulation of Single phase full converter using RL and E loads.
7. PSPICE &MATLAB Simulation of Three phase full converter using RL and E loads.
8. PSPICE &MATLAB Simulation of Single phase AC Voltage controller using RL load.
9. PSPICE &MATLAB Simulation of Three phase inverter with PWM controller.
10. PSPICE &MATLAB Simulation of resonant pulse commutation circuit.
11. PSPICE &MATLAB Simulation of impulse commutation circuit.

### Course Learning Outcome:

The students will be able to:

- Understand the simulations study of stability of system.
- Write a program for steady state and transient stability analysis of a power system.
- Understand the simulation for load frequency control of a single-area and two area systems.
- Understand the simulation analysis of various power electronic converters with different loads.
P14PE109 SEMINAR

Class: M.Tech. I Semester                                    Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| End Semester Exam | : 100 marks |

The candidate should give an oral presentation before the Departmental Post-Graduate Review Committee (DPGRC) on any selected topic relevant to their specialization.

The students will submit a brief report as per specified format and present before the evaluation committee.

The seminar evaluation will be based on the day to day work report submission and presentation before the evaluation committee.
P14PE201  POWER ELECTRONIC CONTROL OF A.C. DRIVES
Class: M.Tech. II Semester  Branch: Power Electronics

Teaching Scheme:  Examination Scheme:
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Continuous Internal Evaluation : 40 marks
End Semester Exam : 60 marks

Course Learning Objectives:
- To draw torque speed characteristics for different control parameters by their equivalent circuit analysis.
- To know different slip recovery drive schemes for speed control of I.M. at rotor side.
- To study Victor control of Induction Motor Drive.
- To study and draw characteristics of synchronous motor using UPF and constant flux linkage control.
- To speed Control of variable Reluctance motor drive and brushless DC motor drive.

UNIT - I (12)

UNIT - II (12)
UNIT – III (12)


UNIT – IV (12)

Variable Reluctance and Brushless DC Motor drives: Variable Reluctance motor drive - Torque production in the variable reluctance motor Drive characteristics and control principles - Current control variable reluctance motor service drive. Brushless DC Motor drives: Three phase full wave Brushless dc motor – Sinusoidal type of Brushless dc motor- current controlled Brushless dc motor Servo drive

References Books:
3. MD Murthy, “Power Electronics and Control of AC Motors” FG Turn Bull pergman Press (For Chapters II, III, V ) 1st Edn.,
4. BK Bose, “Power Electronics and AC Drives” Prentice Hall Eagle Wood Diff New Jersey (for chapters I, II, IV ) - 1st Edn.,

Course Learning Outcomes:
At the end of the course the student will be able to:
- Implement sine-triangle and Space vector PWM techniques on analog and digital platforms.
- Understand and simulate the behavior of high performance induction motor drives using the principles of Vector Control and DTC.
P14PE202 DIGITAL SIGNAL PROCESSOR

Class: M.Tech. II Semester Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation : 40 marks |
| End Semester Exam : 60 marks |

Course Learning Objectives:
- To write Assembly Language Programs for the Digital Signal Processors
- To configure and use digital Input/Output lines and ADCs
- To configure and use Interrupts for real-time control applications
- To configure and use Event Managers for PWM generation

UNIT-I (12)
Introduction to the TMS320LF2407 DSP Controller:
Basic architectural features - Physical Memory - Software Tools
C2xx DSP CPU and Instruction Set:
Introduction & code Generation - Components of the C2xx DSP core - Mapping
External Devices to the C2xx core - peripheral interface - system configuration
registers - Memory - Memory Addressing Modes - Assembly Programming Using the
C2xx DSP Instruction set

UNIT-II (12)
General Purpose Input/Output (GPIO) Functionality:
Pin Multiplexing (MUX) and General Purpose I/O Overview - Multiplexing - General
Purpose I/O control registers - Using the General Purpose I/O Ports

UNIT-III (12)
Interrupts on the TMS320LF2407:
Introduction to Interrupts - Interrupt Hierarchy - Interrupt Control Registers -
Initializing and Servicing Interrupts in Software
The Analog-to-Digital Converter (ADC):
ADC Overview - Operation of the ADC and programming modes

UNIT-IV (12 Hrs)
The Event Managers (EVA - EVB):
Overview of the Event Manager - Event Manager Interrupts - General Purpose Timers -
Compare Units - Capture Units and Quadrature Encoded Pulse (QEP) -General Event
Manager Information - PWM signal Generation with Event Manager
Reference Books:
   Application Notes from the webpage of Texas Instruments

Course Learning Outcomes:
At the end of the course the student will be able to:
- Understand the Assembly Language Programs for the Digital Signal Processors
- Configure and use digital Input/Output lines and ADCs
- Configure and use Interrupts for real-time control applications
- Configure and use Event Managers for PWM generation
P14PE203 ADVANCED POWER ELECTRONICS

Class: M.Tech. II Semester  Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

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<td>End Semester Exam</td>
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Course Learning Objectives:

- To Model existing and modified power converters under small signal and steady state condition
- To Develop power converters with better performance for challenging applications
- To Analyze and design power converters and feedback loops
- To Analyze power quality problems and suggest solutions

UNIT-I (12)

Resonant Converters:
Introduction - Basic resonant circuit concepts - Classification - Load resonant converters - Resonant switch converters - Zero voltage switching clamped voltage converters - Resonant DC link inverters High frequency link integral half cycle converters - Phase modulated resonant converters.

UNIT-II (12)

Modeling of DC-DC Converters:
Basic ac modeling approach - State space averaging - Circuit averaging and averaged switch modeling - Canonical circuit modeling - Converter transfer functions for buck - boost and buck-boost topologies.

Current Mode Control:
Introduction - types - advantages and disadvantages - Slope compensation - Determination of duty cycle and transfer functions for buck - boost and buck-boost converters.

UNIT-III (12)

Design of Switching Power Converters:
Controller Design: Introduction - mechanism of loop stabilization - Shaping E/A gains vs frequency characteristics - Conditional stability in feed-back loop - Stabilizing a continuous mode forward and fly-back converter - Feed-back loop stabilization with current mode control - right plane zero.
Design of Power Converters Components: Design of magnetic components-design of transformer - Design of Inductor and current transformer - Selection of filter capacitors - Selection of ratings for devices - input filter design - Thermal design.
## UNIT-IV (12)

**Power Quality Issues:**
Introduction - Study and design of series - shunt and hybrid compensators - Single and three phase power factor correction.

**Reference Books:**
1. M.H.Rashid, “Power Electronics-Circuits, Devices & Applications” PHI.
4. IEEE Publications on Power Electronics

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P14PE204 ARTIFICIAL INTELLIGENCE APPLICATIONS IN ELECTRICAL ENGINEERING

Class: M.Tech. II Semester
Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : | 40 marks |
|-------------------------------| : | End Semester Exam | : | 60 marks |

Course Learning Objectives:
- To understand properties & compositions of neural networks and learning process.
- To understand methods of minimization like LMS algorithm, back propagation algorithms, single & multi layer perceptions, self organized maps.
- To learn the use of Fuzzy logic and fuzzy system implementation
- To learn and understand associate memories.

UNIT – I (12)


Essentials of Artificial Neural Networks: Artificial Neuron Model, Operations of Artificial Neuron, Types of Neuron Activation Function, ANN Architectures, Classification Taxonomy of ANN – Connectivity, Neural Dynamics (Activation and Synaptic), Learning Strategy (Supervised, Unsupervised, Reinforcement), Learning Rules, Types of Application.


UNIT – II (12)

Multilayer Feed forward Neural Networks: Credit Assignment Problem, Generalized Delta Rule, Derivation of Backpropagation (BP) Training, Summary of Backpropagation Algorithm, Kolmogorov Theorem, Learning Difficulties and Improvements.

UNIT – III (12)

UNIT – IV (12)
Fuzzy Logic System Components and Applications Fuzzification, Membership value assignment, development of rule base and decision making system, Defuzzification to crisp sets, Defuzzification methods. Applications: Neural network applications: Process identification, Function Approximation, control and Process Monitoring, fault diagnosis and load forecasting. Fuzzy logic applications: Fuzzy logic control and Fuzzy classification in power and control applications.

Text Books:

Reference Books:
3. Simon Hykins, “Neural Networks” Pearson Education
5. Bork Kosko, “Neural Networks and Fuzzy Logic System”, PHI Publications

Course Learning Outcomes:
At the end of the course the student will be able to:
- Identify the application domain, wherein the conventional controllers can be replaced with ANNs and Fuzzy Systems.
- Synthesize ANNs and Fuzzy systems to derive the functionality needed to implement the control strategies, relevant to power converters and drives.
- Evaluate and quantify the advantages offered by ANNs and Fuzzy systems over the conventional control strategies.
P14PE205 A DIGITAL CONTROL SYSTEMS
(Elective-III)

Class: M.Tech. II Semester  Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | 40 marks |
| End Semester Exam | 60 marks |

Course Learning Objectives:
- To learn Z-transform of a function and mapping between S-plane and Z-plane.
- To learn the properties and computation of state transition matrix.
- To understand the stability analysis of closed loop system in Z-plane.
- To know the designing of state feedback controller.

UNIT – I (12)

SAMPLING AND RECONSTRUCTION Introduction sample and hold operations, Sampling theorem, Reconstruction of original sampled signal to continuous-time signal.


Z-PLANE ANALYSIS OF DISCRETE-TIME CONTROL SYSTEM Z-Transform method for solving difference equations; Pulse transforms function, block diagram analysis of sampled – data systems, mapping between s-plane and z-plane: Primary strips and Complementary Strips.

UNIT – II (12)

STATE SPACE ANALYSIS State Space Representation of discrete time systems, Pulse Transfer Function Matrix solving discrete time state space equations, State transition matrix and it’s Properties, Methods for Computation of State Transition Matrix, Discretization of continuous time state – space equations

CONTROLLABILITY AND OBSERVABILITY Concepts of Controllability and Observability, Tests for controllability and Observability. Duality between Controllability and Observability, Controllability and Observability conditions for Pulse Transfer Function.
UNIT - III (12)

DESIGN OF DISCRETE TIME CONTROL SYSTEM BY CONVENTIONAL METHODS

UNIT - IV (12)
STATE FEEDBACK CONTROLLERS AND OBSERVERS Design of state feedback controller through pole placement – Necessary and sufficient conditions, Ackerman’s formula.
State Observers – Full order and Reduced order observers.
Linear Quadratic Regulators
Min/Max principle, Linear Quadratic Regulators, Kalman filters, State estimation through Kalman filters, introduction to adaptive controls.

Text Books:

Reference Books:

Course Learning Outcomes:
At the end of the course the student will be able to:
- Evaluate the output of a digital system for a given input.
- Describe the dynamics of a Linear, Time Invariant and Causal digital systems through difference equations.
- Analyze digital systems using the Z-transformation
- Design digital controllers for Power Electronics Systems
P14PE205 B POWER QUALITY
(Elective-III)

Class: M.Tech. II Semester  Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : 40 marks |
| End Semester Exam | : 60 marks |

Course Learning Objectives:

- To understand the effect of nonlinear loads and disturbances on sensitive loads.
- To know the standards and classification of power quality disturbances.
- To known the causes and effects of interruptions.
- To understand the concepts of causes and measurement of voltage sag.
- To get knowledge on effects and mitigation of voltage sag.

UNIT – I (12)

Introduction: Introduction of the Power Quality (PQ) problem, Terms used in PQ: Voltage, Sag, Swell, Surges, Harmonics, over voltages, spikes, Voltage fluctuations, Transients, Interruption, overview of power quality phenomenon, Remedies to improve power quality, power quality monitoring.

UNIT – II (12)


Short Interruptions: Short interruptions – definition, origin of short interruptions, basic principle, fuse saving, voltage magnitude events due to re-closing, voltage during the interruption, monitoring of short interruptions, difference between medium and low voltage systems. Multiple events, single phase tripping – voltage and current during fault period, voltage and current at post fault period, stochastic prediction of short interruptions.

UNIT – III (12)

Voltage sag – characterization – Single phase: Voltage sag – definition, causes of voltage sag, voltage sag magnitude, monitoring, theoretical calculation of voltage sag magnitude, voltage sag calculation in non-radial systems, meshed systems, voltage sag duration.

Voltage sag – characterization – Three phase: Three phase faults, phase angle jumps, magnitude and phase angle jumps for three phase unbalanced sags, load influence on voltage sags.

UNIT – IV (12 Hrs)
Mitigation of Interruptions and Voltage Sags: Overview of mitigation methods – from fault to trip, reducing the number of faults, reducing the fault clearing time changing the power system, installing mitigation equipment, improving equipment immunity, different events and mitigation methods. System equipment interface – voltage source converter, series voltage controller, shunt controller, combined shunt and series controller.

Power Quality and EMC Standards: Introduction to standardization, IEC Electromagnetic compatibility standards, European voltage characteristics standards, PQ surveys.

Reference Book:

Course Learning Outcomes:
At the end of the course the student will be able to:
- Implement compensating techniques for a given power quality problem.
- Suggest protection techniques under different fault conditions.
- Develop control techniques for compensating devices.
P14PE205 C MICROPROCESSOR & MICROCONTROLLERS (Elective-III)

Class: M.Tech. II Semester  Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

- Continuous Internal Evaluation : 40 marks
- End Semester Exam : 60 marks

Course Learning Objectives:

- To learn importance of microprocessors and microcontrollers.
- To learn and understand architecture and programming of 8086 processor.
- To learn and understand interfacing techniques like memory and I/O interfacing.
- To learn the various data transfer techniques like programmed I/O, interrupt I/O and direct memory access.
- To learn and understand architecture of advanced processors.

UNIT – I (12)


UNIT – II (12)

Advanced Microprocessors: Intel 80386 programming model, memory paging, Introduction to 80486, Introduction to Pentium Microprocessors and special Pentium pro features. Basic peripherals & Their Interfacing:-Memory Interfacing (DRAM) PPI-Modes of operation of 8255, interfacing to ADC & DAC.

UNIT – III (12)

Special Purpose of Programmable Peripheral Devices and Their interfacing:-Programmable interval timer, 8253, PIC 8259A, display controller Programmable communication Interface 8251, USART and Exercises.

UNIT – IV (12)

Microcontrollers: Introduction to Intel 8-bit &16-bit Microcontrollers, 8051-Architecture, Memory organization, Addressing Modes and exercises. Hardware description of 8051: Instruction formats Instruction sets, interrupt Structure & interrupt priorities, Port structures &Operation linear counter Functions different Modes of Operation and Programming examples.
Text Books:

Reference Books:
1. Douglas V. Hall, “Microprocessors & Interfacing Programming & Hardware”
2. Prof. C.R. Sarma, “Microprocessors & Microcontrollers”

Course Learning Outcomes:
At the end of the course the student will be able to:
- Understand the architecture of a 8086 microprocessor.
- Write the program by using 8086-assembly language by understanding the instruction set and addressing modes.
- Configure the interrupt structures and to use interrupt sub routines to implement real-time control.
- Interface the microprocessor with memory and I/O subsystems.
P14PE205 D  APPLICATIONS OF POWER CONVERTERS
(Elective-III)

Class: M.Tech. II Semester  Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : 40 marks |
| End Semester Exam | : 60 marks |

Course Learning Objectives:
- To analyze the Power Electronic Application requirements.
- To identify suitable power converter from the available configurations.
- To develop improved power converters for any stringent application requirements.
- To improvise the existing control techniques to suit the application

UNIT-I (12)
Overview of the course- Power converter topologies for Induction heating- Welding-Lighting

UNIT-II (12)
High voltage power supplies - power supplies for X-ray applications - power supplies for radar applications - power supplies for space applications - Low voltage high current power supplies

UNIT-III (12)
Power Conditioners – UPS - Active Power Filters - Shunt active power filters - Series active power filters - Hybrid active power filters - UPQC

UNIT-IV (12)
Electric Vehicle – batteries, chargers, inverters, bi-directional DC-DC converters, motors, Automotive Electronics

Reference Books:

Course Learning Outcomes:
At the end of the course the student will be able to:
- Analyze the Power Electronic Application requirements.
- Identify suitable power converter from the available configurations.
- Develop improved power converters for any stringent application requirements.
- Improvise the existing control techniques to suit the application
P14PE206 A ELECTRICAL MACHINE DESIGN
(Elective-IV)

Class: M.Tech. II Semester  Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

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<td>End Semester Exam</td>
<td>60 marks</td>
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Course Learning Objectives:
- To carryout optimal design of electrical machinery with due considerations for magnetic and electric circuits.
- To evaluate and judge the options available for the thermal management of electrical machines.
- To convert the generic design procedures into computer aided methodologies

UNIT-I (12)
Thermal circuit: Enclosures-types, ventilation and cooling methods in electrical machines and transformers

UNIT-II (12)

UNIT-III (12)
DC machines: Output equation and coefficient, design of pole. Transformer: Output coefficient and equation, window dimensions.

UNIT-IV (12)
Induction motor: Output equation, main dimensions calculation.
Alternators: Output coefficient and calculation of main dimensions, short circuit ratio.

Text Books:

Course Learning Outcomes:
At the end of the course the student will be able to:
- Carryout optimal design of electrical machinery with due considerations for magnetic and electric circuits.
- Evaluate and judge the options available for the thermal management of electrical machines.
- Convert the generic design procedures into computer aided methodologies.
P14PE206 B ELECTRIC SMART GRID (Elective-IV)

Class: M.Tech. II Semester

Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

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<td>To learn the concept of smart grid- Electricity network.</td>
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<td>To know the dc distribution &amp; smart grid.</td>
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<tr>
<td>To know the understand of Energy system concept.</td>
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<tr>
<td>To learn the efficient electric end-use technology alternatives.</td>
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Continuous Internal Evaluation : 40 marks
End Semester Exam : 60 marks

UNIT–I (12)

INTRODUCTION

SMART GRID TO EVOLVE A PERFECT POWER SYSTEM: Introduction- Overview of the perfect power system configurations- Device level power system- Building integrated power systems- Distributed power systems- Fully integrated power system- Nodes of innovation.

DC DISTRIBUTION AND SMART GRID
AC vs DC sources-Benefits of and drives of DC power delivery systems-Powering equipment and appliances with DC-Data centers and information technology loads-Future neighborhood Potential future work and research.

INTELLIGRID ARCHITECTURE FOR THE SMARTGRID: Introduction- Launching intelligrid today- Smart grid vision based on the intelligrid architecture- Barriers and enabling technologies.

UNIT–II (12)

ENERGY SYSTEMS CONCEPT
Smart energy efficient end use devices-Smart distributed energy resources-Advanced whole building control systems- Integrated communications architecture-Energy management-Role of technology in demand response- Current limitations to dynamic energy management-Distributed energy resources-Overview of a dynamic energy management-Key characteristics of smart devices- Key characteristics of advanced whole building control systems-Key characteristics of dynamic energy management system.
UNIT-III (12)
ENERGY PORT AS PART OF THE SMART GRID:
Concept of energy -Port, generic features of the energy port.
Policies and programs to encourage end - use energy efficiency: Policies and programs in action - multinational - national-state-city and corporate levels.

UNIT-IV (12)
Efficient electric end - use technology alternatives

Text Books:

Course Learning Outcomes:
At the end of the course the student will be able to:
- Understand the concept of smart grid- Electricity network.
- Understand the dc distribution & smart grid.
- Understand the energy system concept.
- Understand the efficient electric end-use technology alternatives.
P14PE206 C DIGITAL SIGNAL PROCESSOR CONTROLLED DRIVES  
(Elective-IV) 
Class: M.Tech. II Semester  
Branch: Power Electronics 

Teaching Scheme: 
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Examination Scheme: 
| Continuous Internal Evaluation | 40 marks |
| End Semester Exam | 60 marks |

Course Learning Objectives: 
- To Interface the DSP platform with sensors such as hall-effect voltage sensors, hall-effect current sensors, shaft encoder for data acquisition for motor drive applications. 
- To Scale and normalize the data to suit the requirements of the drive system. 
- To Exploit the architectural features of the DSP platform to design and implement algorithms for the realization of controllers, Pulse Width Modulators and observers. 

UNIT-I (12) 
Overview of TMSLF2407 DSP controller: Instruction Set, Interrupts. Clarke's and park's transformations. 

UNIT-II (12) 
Implementation of Clarke's and Park's transformation. 

UNIT-III (12) 
SVPWM, BLDC Motor Control System, permanent magnet synchronous machines control system, 

UNIT-IV (12) 
Vector control of IM, field oriented control, Induction Motor Speed Control using LF2407 DSP. 

Text Books: 

Course Learning Outcomes: 
At the end of the course the student will be able to: 
- Interface the DSP platform with sensors such as hall-effect voltage sensors, hall-effect current sensors, shaft encoder for data acquisition for motor drive applications. 
- Scale and normalize the data to suit the requirements of the drive system. 
- Exploit the architectural features of the DSP platform to design and implement algorithms for the realization of controllers, Pulse Width Modulators and observers.
P14PE206 D  FLEXIBLE A.C. TRANSMISSION SYSTEMS
Class: M.Tech. II Semester  Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : 40 marks |
| End Semester Exam              | : 60 marks |

Course Learning Objectives:
- To learn the power flow in transmission system concepts.
- To learn the operations of application voltage source converters.
- To learn the Objectives of shunt & Series compensation.
- To learn the behavior of various FACTS devices.

UNIT – I (12)
FACTS Concepts: Transmission interconnections power flow in an AC system, loading capability limits, Dynamic stability considerations, importance of controllable parameters, basic types of FACTS controllers, benefits from FACTS controllers.

UNIT – II (12)
Voltage Source Converters: Single phase three phase full wave bridge converters, transformer connections for 12 pulse 24 and 48 pulse operation. Three level voltage source converter, pulse width modulation converter, basic concept of current source Converters, comparison of current source converters with voltage source converters.

UNIT – III (12)
Static Shunt Compensation: Objectives of shunt compensation, mid point voltage regulation, voltage instability prevention, improvement of transient stability, Power oscillation damping, Methods of controllable var generation, variable impedance type static var generators, switching converter type var generators, hybrid var generators.

SVC and STATCOM: Regulation and slope transfer function and dynamic performance, transient stability enhancement and power oscillation damping operating point control and summary of compensator control.

UNIT – IV (12)
Static Series Compensators: Concept of series capacitive compensation, improvement of transient stability, power oscillation damping Functional requirements. GTO thyristor controlled series capacitor (GSC), thyristor switched series capacitor (TSSC), and thyristor controlled series capacitor (TCSC) control schemes for GSC TSSC and TCSC.
**Text Book:**

**Course Learning Outcomes:**
At the end of the course the student will be able to:
- Understand the role of impedance control, phase angle control and voltage control in controlling real and reactive power in transmission systems.
- Identify configuration of FACTS controller required for a given application.
P14PE207 ELECTRIC DRIVES LABORATORY

Class: M.Tech. II Semester
Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : 40 marks |
| End Semester Exam               | : 60 marks |

Course Learning Objectives:

- To learn the advantages of closed loop control of electrical machines.
- To learn the control techniques for two and four quadrant operations of DC drives.
- To learn the speed control methods of induction motor.
- To learn the application of power electronic converters voltage control.

List of Experiments:

1. Speed Measurement and closed loop control using PMDC motor
2. Thyristorised drive for PMDC Motor with speed measurement and closed loop control.
3. IGBT used single 4 quadrant chopper drive for PMDC motor with speed measurement and closed loop control.
4. Thyristorised drive for 1Hp DC motor with closed loop control.
5. 3 Phase input, thyristorised drive, 3 Hp DC motor with closed loop
6. 3 Phase input IGBT, 4 quadrant chopper drive for DC motor with closed loop control equipment.
7. Cycloconverter based AC Induction motor control equipment.
8. Speed control of 3 phase wound rotor Induction motor.
10. Single phase half wave controlled converter with inductive load.

Course Learning Outcomes:

At the end of the course the student will be able to:

- To understand the advantages of closed loop control of electrical machines.
- To understand the control techniques for two and four quadrant operations of DC drives.
- To understand the speed control methods of induction motor.
- To understand the application of power electronic converters voltage control.
P14PE208 DIGITAL SIGNAL PROCESSORS LABORATORY

Class: M.Tech. II Semester

Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | : 40 marks |
| End Semester Exam              | : 60 marks |

Course Learning Objectives:

- To learn understand the architecture and addressing modes of the DSP TMS320LF2407A
- To learn use the instruction set of the DSP TMS320LF2407A for writing programs
- To learn use the event manager of DSP TMS320LF2407A for PWM generation
- To learn configure the interrupts and use the digital I/Os-ADC and DAC

List of Experiments:

1) General programming examples such as:
   a) Arithmetic and logical operations
   b) Direct & Indirect Data Transfer
   c) Generation of Ramp-up and Ramp-down wave
   d) Sorting of Data in Ascending order and descending order
   e) Square root of positive integer
   f) Maximum and minimum of given set of numbers
2) Study of aliasing phenomenon.
3) Study of the On-Chip Analog to Digital conversion of TMS320LF2407 processor.
4) Study of the GPIO registers
5) Producing a saw tooth wave from the DAC
6) PWM signal generation using event manager
7) Space Vector Modulation of a 3-Phase VSI
8) Implementation of the difference equation
9) Study of the interrupts of TMS320LF2407 processor
10) Dead time generation to avoid shoot-through fault

Course Learning Outcomes:

At the end of the course the student will be able to:

- To understand the architecture and addressing modes of the DSP TMS320LF2407A
- To use the instruction set of the DSP TMS320LF2407A for writing programs
- To use the event manager of DSP TMS320LF2407A for PWM generation
- To configure the interrupts and use the digital I/Os-ADC and DAC
P14PE209 COMPREHENSIVE VIVA

Class: M.Tech. II Semester  
Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| End Semester Exam | : | 100 marks |

Course Learning Objectives:

- To learn comprehend and correlate the understanding of various courses in design and operation of modern power electronic & drive systems.

The Viva includes question from all the subjects of first and second semesters with more emphasis on Power Electronics Concepts.

Course Learning Outcomes:

At the end of the course the student will be able to:

- Comprehend and correlate the understanding of various courses in design and operation of modern power electronic & drive systems.
P14PE301 INDUSTRIAL TRAINING

Class: M.Tech. III Semester
Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

End Semester Exam : 100 marks

Course Learning Objectives:
- To learn advanced topics in power electronics & drives.
- To Improve language and communication skills
- To Enrich employability & entrepreneurial skills by providing necessary inputs in line with global vision through Industry-Institute Interaction.

The candidate should submit the report and present talk on the training undergone highlighting the contents of the Report before the Departmental Post-Graduate Review Committee (DPGRC).

Course Learning Outcomes:
At the end of the course the student will be able to:
- Understand advanced topics in power electronics & drives.
- Improve language and communication skills
- Enrich employability & entrepreneurial skills by providing necessary inputs in line with global vision through Industry-Institute Interaction.
P14PE302 DISSERTATION

Class: M.Tech. III Semester                  Branch: Power Electronics

Teaching Scheme:

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Examination Scheme:

| Continuous Internal Evaluation | 40 marks |
| End Semester Exam              | 60 marks |

Course Learning Objectives:

- To learn Recognize and formulate a problem to analyze, synthesize, evaluate, simulate and create a power electronic converter and / or a drive system.
- To learn carryout modeling and simulation studies pertaining to the system and prepare a presentation.

The candidate will chose the topic of the Project Work in consultation with the Guide allotted. A report in the prescribed format is to be submitted that includes extensive survey of literature on the topic, highlighting the scope of the work. It should also state the methodology to be adopted and work involved in different modules of the Project Work. The report should clearly specify the expected outcome.

The candidate should submit the report and present talk on the work done, highlighting the contents of the Report before the Departmental Post-Graduate Review Committee (DPGRC).

Course Learning Outcomes:
At the end of the course the student will be able to:

- Recognize and formulate a problem to analyze, synthesize, evaluate, simulate and create a power electronic converter and / or a drive system.
- Carryout modeling and simulation studies pertaining to the system and prepare a presentation.
P14PE401  DISSENTATION & VIVA-VOCE

Class: M.Tech. IV Semester          Branch: Power Electronics

Teaching Scheme:
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Examination Scheme:
- Continuous Internal Evaluation : 40 marks
- End Semester Exam : 60 marks

Course Learning Objectives:
- To learn build the hardware to demonstrate the principle of working.
- To learn correlate the analytical, simulation and experimental results.
- To learn deduce conclusions and draw inferences worthy of publication.

The candidate should submit the report and present talk on the work done, highlighting the conclusions drawn and outcome of the work before the Departmental Post-Graduate Review Committee (DPGRC).

Course Learning Outcomes:
At the end of the course the student will be able to:
- Build the hardware to demonstrate the principle of working.
- Correlate the analytical, simulation and experimental results.
- Deduce conclusions and draw inferences worthy of publication.