Registration Form
for
A Two-Day Workshop
on
Recent Advances in VLSI
8th & 9th November 2014.

Name:_____________________________________
Designation:_______________________________
Organization:_______________________________
Correspondence Address:______________________________
_PIN code______________________________
Phone #:______________________________
E-mail:_____________________________________

Payment Details:
D.D.No. & Date______________________________
Amount Rs.______________________________
Name of the Bank______________________________

Accommodation Required: Yes/No
Category: Academic/Industry/others

Signature of the Participant
Date:__________________________
Place:__________________________

Signature of the Head of the Institute/
Sponsoring Authority
(With Date and Seal)

Registration Fee
Rs 400/- .... for PG Students / Research scholars
Rs 500/- .... for Participants from academic institutions
Rs 750/- .... for Participants from industry and Others
The registration fee has to be paid through
DD drawn in favor of “Principal, KITS, Warangal” payable at
Warangal.

Spot Registrations are accepted

Chief Patron:
Sri P. Ganga Reddy, President, EES, Warangal
Patron:
Capt. V. Lakshmikantha Rao,
Secretary & Correspondent, KITS Warangal (KITSW)
Chairman:
Dr. K. Ashoka Reddy, Principal, KITSW
Convener:
G. Raghotham Reddy, Head, Dept. of ECE, KITSW
Co-ordinators:
V. Raju, Asst. Professor, Dept. of ECE, KITSW
R. Srikanth, Asst. Professor, Dept. of ECE, KITSW
Organizing Committee Members:
• Dr. K. Sivani, Professor & Dean (R&D)
• S.P. Girija, Assoc. Professor
• E. Suresh, Assoc. Professor
• B. Rama Devi, Assoc. Professor
• A. Vijaya, Assoc. Professor
• B. Komuraiah, Asst. Professor
• Ch. Navitha, Asst. Professor
• A. Srinivas, Asst. Professor
• K. Ramudu, Asst. Professor
• Syed Zacheruddin, Asst. Professor
• B. Narasimha, Asst. Professor
• P. Chiranjeevi, Asst. Professor
• D. Venu, Asst. Professor
• J. Sheshagiri Babu, Asst. Professor
• S. Pradeep Kumar, Asst. Professor
• P. Yugender, Asst. Professor
• S. Narender, Asst. Professor
• A. Pavan, Asst. Professor
• Ch. Pavan Kumar, Asst. Professor
• E. Susmitha, Asst. Professor

Accommodation Required: Yes/No
Category: Academic/Industry/others

Signature of the Participant
Date:__________________________
Place:__________________________

Signature of the Head of the Institute/
Sponsoring Authority
(With Date and Seal)
ABOUT THE INSTITUTE:

Kakatiya Institute of Technology & Science, popularly known as KITS, was established in the year 1980. KITS is the flag ship institute sponsored by Ekasila education Society (EES), Warangal a pale philanthropic society, whose primary objects to establish and manage a variety of educational institutions with highest quality and thus to provide quality human resources in the service of society. The institute is permanently affiliated to Kakatiya university, Warangal and recognized by AICTE & UGC New Delhi. UGC, New Delhi and KU, WGL have granted Autonomous Status to KITS, Warangal.

The institute has its own campus covering an area of 67 acres of land. It is offering undergraduate courses in seven branches of engineering and seven post graduate programs. It is accredited by NBA and the institute has conducted several seminars, symposiums, conferences in the past.

The institute is located on New delhi-Chennai broad gauge railway line and is well connected to other cities by rail and road.

Warangal city is just 150 kms from Hyderabad, capital city of Telangana State(T.S.). The institute is located 13 kms and 20 kms from kazipet and warangal railway stations respectively, on Warangal-Karimnagar state highway.

ABOUT ECE DEPARTMENT:

The department of Electronics and Communication Engineering (ECE) was established in the year 2000. The current intake in to the B.Tech (ECE) course is of 120 students. The department is accredited by NBA in the year 2008. The department has dedicated and Qualified faculty with 2 Professors, 5 Associate Professors, 18 Assistant Professors with minimum qualification having PG and ten faculty members are pursuing Ph.D in reputed Institutions/Universities.

To expose the students to the current trends in the areas of Electronics and Communication Engineering and allied ones, the department is continuously conducting various national level students technical symposiums under the banner of ELECTROCOM and workshops for faculty of ECE, EEE and E&E in every academic year.

The department has modernized and well equipped laboratories with state-of-art facilities to cater the needs of UG & PG courses. The list of labs is given below:

- Analog Communication Laboratory
- Analog & Digital Simulation Laboratory
- Digital Communication Laboratory
- Microprocessor & Microcontroller Laboratory
- Microwave & Optical Fibre Communication Laboratory
- Pulse & Digital Circuits Laboratory
- Signal Processing Laboratory

ABOUT WARANGAL CITY:

The historical city of Warangal is the 2nd biggest in the state of Telangana and it is an important educational cluster. Once the capital of great Kakatiya Kingdom, Warangal, earlier known as Orugallu or Ekashilanagaram still retains its importance as a culture center in Telangana state. There are many historical monuments in and around the city such as fort Warangal, Thousand Pillar Temple, Badhrakali Temple, Ramappa temple along with beautiful scenic lakes of Ramappa, Pakala and Laknavaram. In addition to this there are wild life sanctuaries nearby and other places for the tourist.

ABOUT THE WORKSHOP:

Technological developments have led to growth of FPGA industry that caters to the needs of medical, defense, commercial and industrial applications. Advances in FPGA architectures and availability of user friendly EDA tools enable engineers to quickly develop applications on FPGAs. Engineering students should learn digital design concept, HDL coding and program FPGAs, however the practical applications of FPGA and its importance in industry requirements are not addressed in the academic. EDA industry provides sophisticated tools and hardware boards for engineers and working professionals to develop applications on FPGA. FPGAs are widely used for implementation of complex applications that are computational intensive and require less computational time.

Developing applications on FPGA requires system level approaches and there are several readymade IPs and modules that are widely adopted in the industry in developing applications. In this workshop we provide solutions to the system level approaches for applications to be developed on FPGA.

Workshop Course Contents:

- Overview of Xilinx FPGA & CPLD Architecture
- Advanced Image Processing Techniques
- Concepts of Core generator IP library
- Hardware debugging using Xilinx Chipscope Pro ILA & VIA
- Xilinx ISE tool flow and hardware implementation
- Writing & Debugging Basic Software Applications Using Xilinx Kit and Targeted on Spartan6E Board

ACCOMMODATION:

Accommodation is available for limited members on first come first serve basis.

Send your Entries:
To
The Convener
A Two-Day Workshop on Recent Advances in VLSI
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